





#### **CYBERSECURITY AT CEA TECH : ASSESSMENTS AND SOLUTIONS**

Alain MERLE, PhD alain.merle@cea.fr

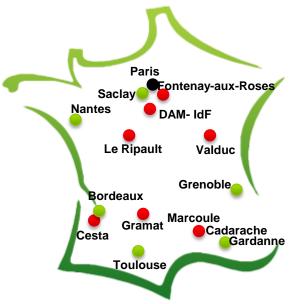
Florent KIRCHNER, PhD Florent.kirchner@cea.fr

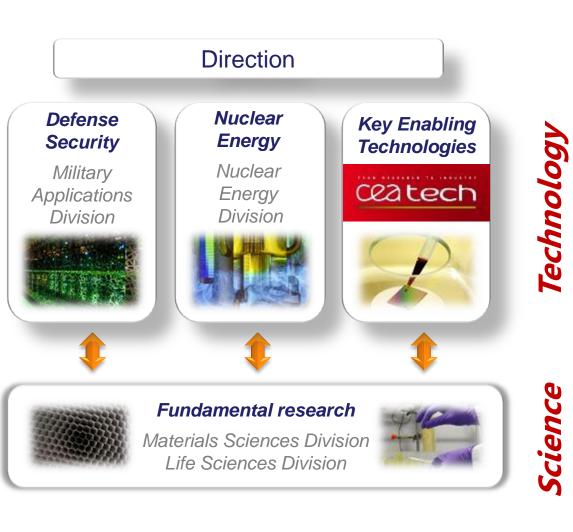
Assia TRIA, PhD Assia.tria@cea.fr



#### **CEA: FROM RESEARCH TO INDUSTRY**

- » 16 000 employees
- » 10 research centers
- » 4 regional extensions
- » Budget of 4.3 billion €
- » 650 patents/year
- » 4000 publications/year
- » 50 Joint Research Laboratory
- » 150 startup creations in 30 years







# **MISSION :** To develop and disseminate new technologies for industry

- Annual operating budget of more than **€500 M**
- More than **50 HIGH-TECH START-UP** over the past 10 years
- 4,500 EMPLOYEES
- **550 PRIORITY PATENT** applications per year par an
- Our CUSTOMERS :
  - ✓ 80 % listed on the CAC 40
  - ✓ More than 500 SMBS
  - ✓ 145 INTERNATIONAL CUSTOMERS





## World-class experts, « application » know-how and equipments

Micro- and nanoelectronics 7,000 sq. m. (clean rooms) Staff: 800 Investment: €1 billion

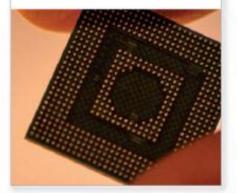


Nanocharacterization

2,500 sq. m. Staff: 80 Investment: €30 million



Design 1,800 sq. m. Staff: 100



Embedded systems 1,200 sq. m. Staff: 180



Batteries 3,000 sq. m. Staff: 200 Investment: €50 million



Solar 25,000 sq. m. Staff: 380 Investment: €150 million



Clinatec 6,000 sq. m. Staff: 100 Investment: €40 million



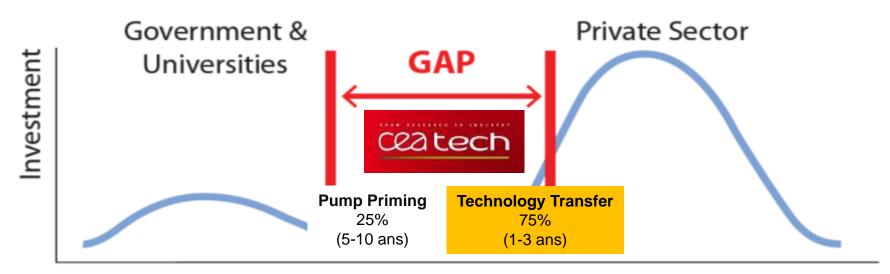
Advanced manufacturing 2,000 sq. m. Staff: 200

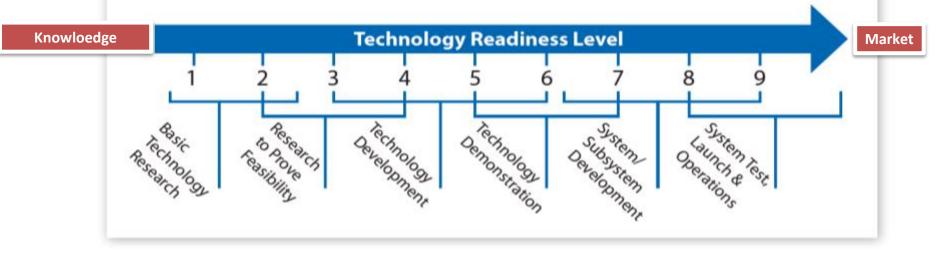




#### CEA TECH: BRINGING COMPETITIVENESS TO OUR CUSTOMERS

## Gap in Manufacturing Innovation





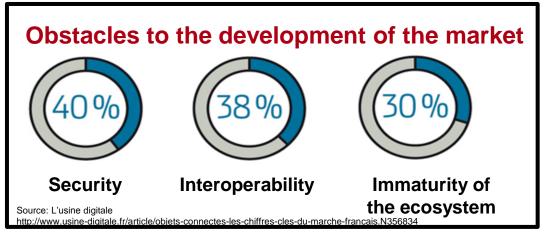


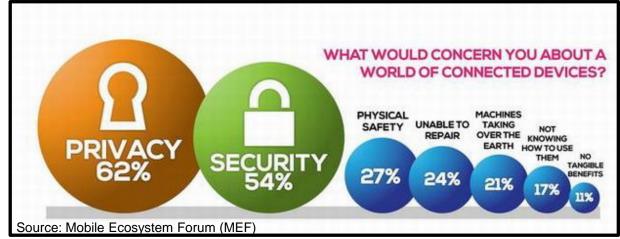
# WHAT IS SECURITY?

- "The quality or state of being secure—to be free from danger"
- A successful organization should have multiple layers of security in place:
  - Physical security
  - Personal security
  - Operations security
  - Communications security
  - Network security
  - Information security



#### **SECURITY: A SOCIETAL CHALLENGE**



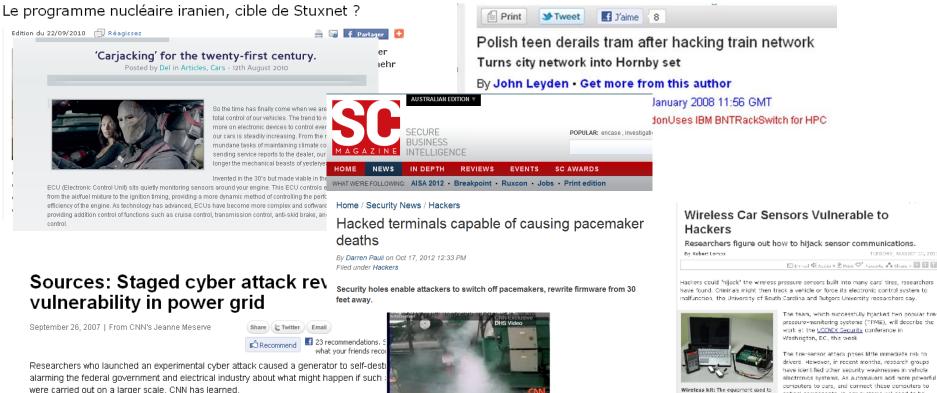


# Massive adoption by citizens relies on confidence on security and privacy



#### WHAT'S NEW: CYBERATTACKS ON THE REAL WORLD

- 2007: Autodestruction of a generator in a power plant
- 2008: A polish teen derails a TRAM.
- 2010: STUXNET worm against the Iranian nuclear program
- 2010: Wireless sensors used for "carjacking"
- 2012: Risks on medical implants (pacemakers)



Wireless kit: The equipment used to hijack a car's tire senses included a laptop, a programmable radio

critical components, in-car systems will need to be

secured against hackers, experts warn

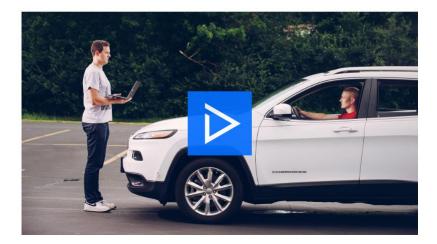


#### **CYBERSECURITY: A RISK FOR SAFETY**

# WIR	E D Hack	ers Remotely Kill	a Jeep on the H	lighway—With M	e in It	SUBSCRIBE 🔎
BUSINESS	CULTURE	DESIGN	GEAR	SCIENCE	SECURITY	TRANSPORTATION

ANDY GREENBERG SECURITY 07.21.15 6:00 AM

## HACKERS REMOTELY KILL A JEEP ON THE HIGHWAY—WITH ME IN IT





Podcasts | Biomedical | Devices

#### **Hacking Pacemakers**

Manufacturers are still not putting security first when designing implantable medical devices

By Steven Cherry Posted 30 Apr 2013 | 15:33 GMT 🕂 Share | 🖂 Email | 🗇 Print



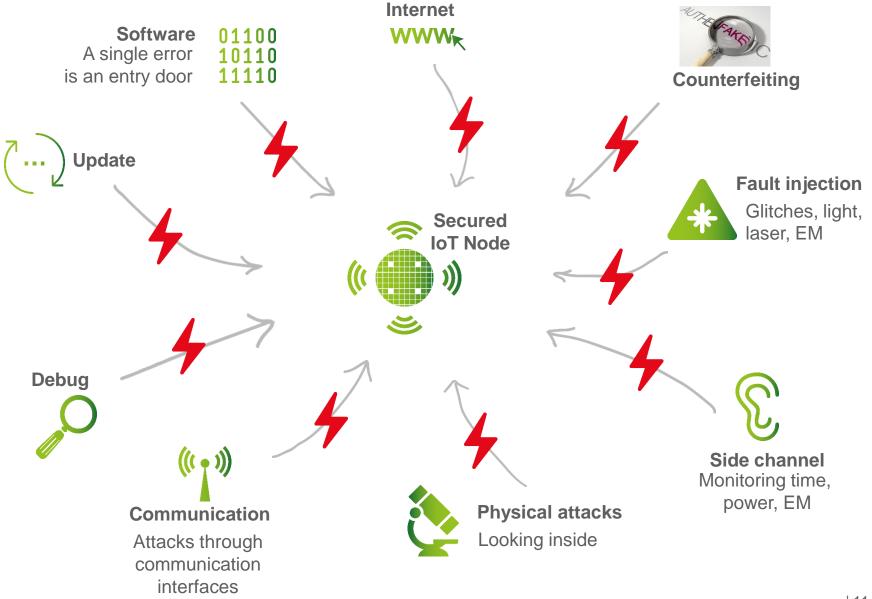


#### **SECURITY: DEFINITIONS**

	Objective	Attack types	Recent attacks	Protection
Confidentiality	Ensuring that information is secret	Intrusion, Worms, hacking,	AREVA, MASTERCARD, SONY,	Cryptography, Smartcards, Dedicated Circuit (TPM)
				Burtout
	Objective	Attack types	Recent attacks	Protection
Integrity	Ensuring that a system is not modified	Worms, trojans…	Payment terminal in UK, Stuxnet	Cryptography, Trusted computing
	Objective	Attack types	Recent attacks	Protection
Availability	Ensuring availability of a system	Denial of service, Anonymous	Estonia, Anonymous	Very difficult ! Some protection for web sites
	Objective	Attack types	Recent attacks	Protection
Authenticity	Ensuring the user/component is the	Cloning	Pay TV, Counterfeiting	Smartcards, Secured devices

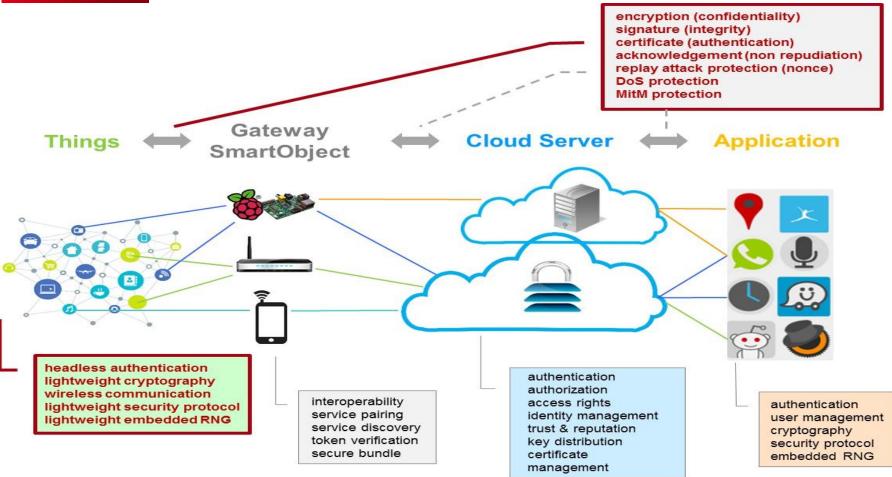
-







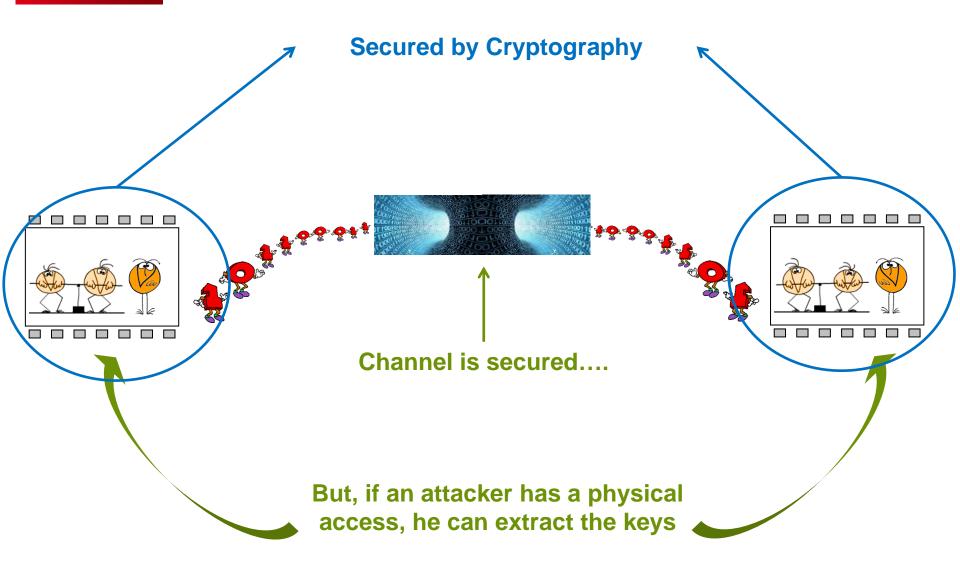
#### **CRYPTOGRAPHY IS COMPLEX**



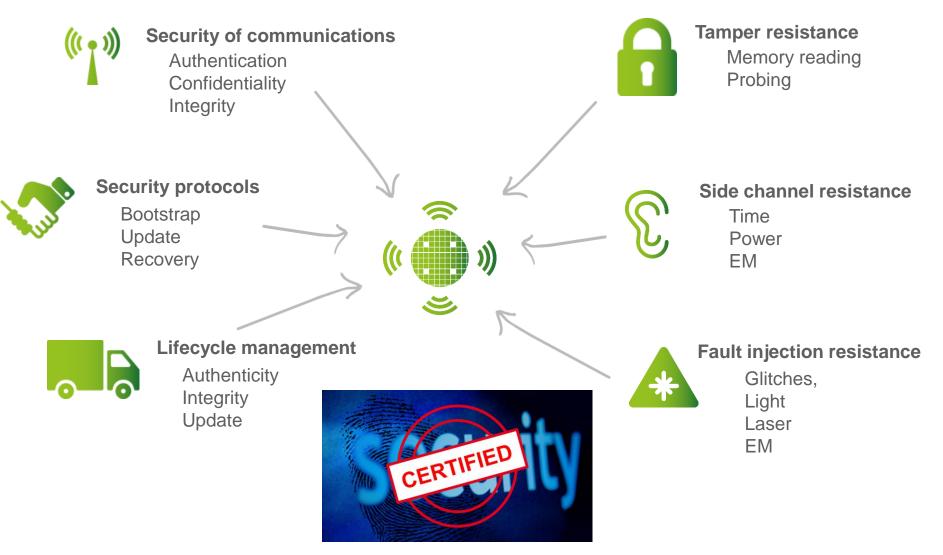
- Key management: Bootstrap, Update, Recovery
- Intrinsic resistance
  - Moore's law: increasing key size (DES, TDES, AES 128, AES 256)
  - Quantum computer : killing asymetric cryptography

### **SECURITY OF COMMUNICATIONS**

Ceatech







## By technology, architecture & embedded SW



#### **NEED FOR A OBJECTIVE MEASURE AND LABEL**

#### Looking backwards

# No Security standard for emerging markets

# Key elements for the future



Efficiency of the Evaluation/Certification schemes for Smartcards





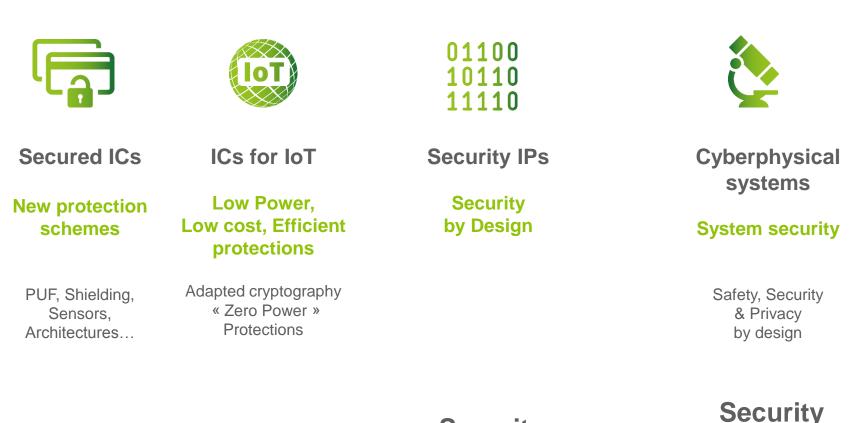
Needs expressed:

- Industrial systems
- Medical devices
- Automotive
- IoT
- Biometrics
- Home appliances
- ...

Standard & trustworthy Certificates



#### **CEA-TECH'S RESEARCH AXIS**



More security Best tradeoff

Security everywhere

Security for everything



#### **INNOVATIVE SOLUTIONS**

#### **OUR PROMISE**

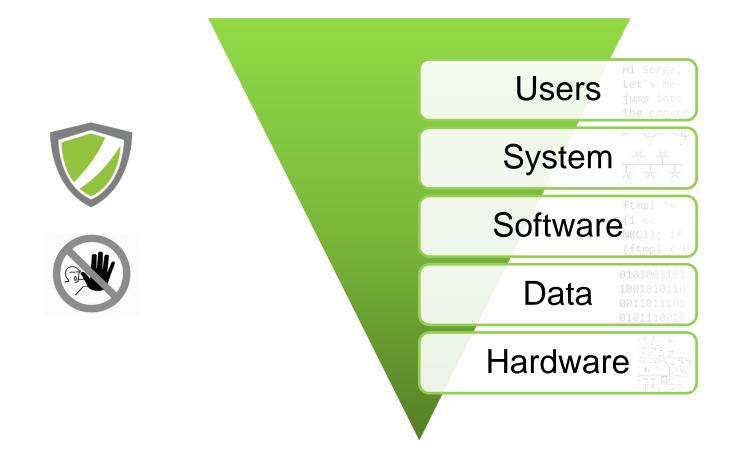
- Establish strong guarantees for the security of systems
- Based on cutting-edge mathematical techniques and reasoning capabilities
- In automotive, avionics, connected objects, drones, health, IT, smart grids, ...

## THE CHANGE WE SEEK TO MAKE

- **Develop** highly innovative solutions to industrial challenges
  - Communication services
  - Intrusion detection systems
  - Cryptographic techniques
  - Data analysis for privacy
  - Source code assessment and verification
  - Malware analyses
  - System-level risk analyses
- **Transfer** next-generation components and tools to technical teams

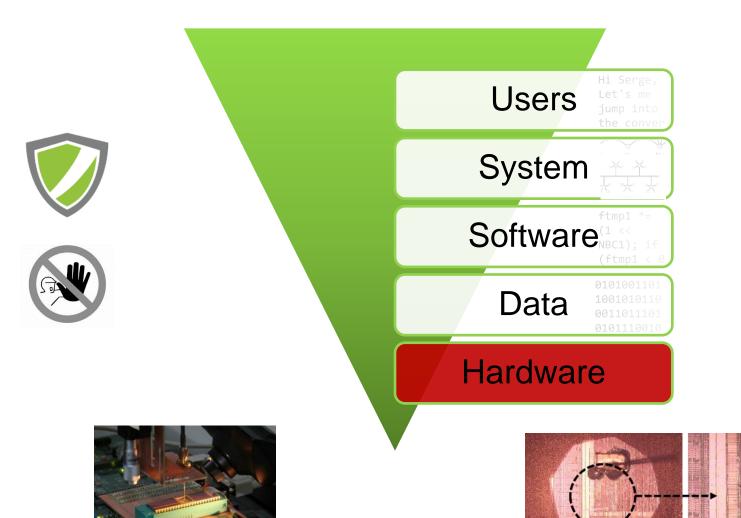
## WE HELP OUR PARTNERS DELIVER HIGH-CONFIDENCE SYSTEMS





"Trustworthy computing (with software) cannot exist until we have trustworthy hardware to build it on" Dr. Dean Collins, Deputy Director, DARPA





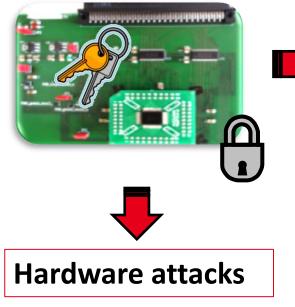


#### **ATTACKS ON SECURE DEVICES**

Cryptanalysis



RC5, MIFARE, Brute force attacks, Etc.





# Software attacks

Buffer overflows, Brute force attacks, Attacks on protocols Etc.

Extremely powerfull thanks to the direct access to the component:

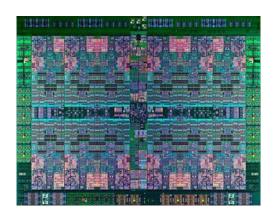


Example: AES-128 key cracking in minutes on a 32-bit <u>unsecure</u> microcontroller

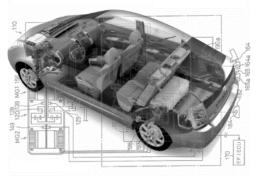


#### HARDWARE SECURITY

- Secure design of COMPONENTS and SYSTEMS
- Strong links with telecommunications
- Tradeoff
  - Security level
  - Power consumption
  - Size / volume
  - Cost



Tamper resistant chip design



Cost effective solution



IoT dedicated cryptography

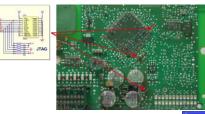


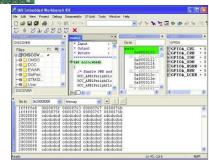
Ultra low cost pairing



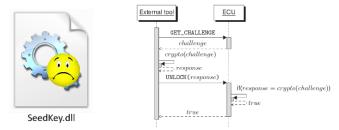
#### **HARDWARE SECURITY**

- Debug ports exploits
  - JTAG, USB, UART, SPI...ports
  - Read/write memory space
  - Access MCU internal registers
  - Control execution
  - Code injection
- Debug port protections
- CAN/CCP exploits
  - The CANApe ECU debug tool
- Alternative boot
  - Exploiting MCU interfaces
  - PCB-level protections
- Firmware protection
  - Cryptographic protections against reverse engineering
  - Software-based code injection protections

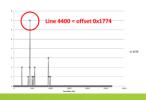








Boot mode s	election pins	Boot mode	Allasing	
BOOT1	BOOT0	Boot mode		
х	0	User Flash memory	User Flash memory is selected as the boot space	
0	1	System memory	System memory is selected as the boot space	
1	1	Embedded SRAM	Embedded SRAM is selected as the boot space	





#### LIGHTWEIGHT CRYPTOGRAPHIC IMPLEMENTATIONS

- Different cryptology methods
  - Elliptic curves, stream ciphers, lattice based, ...
- System integration targets
  - FPGA
  - Microcontrollers
  - Mixed architectures
- Optimizations
  - Time: throughput, latency
  - Digital footprint
  - Memory size
  - Power budget



Demonstrator of embedded cryptography in a contactless card



Implementation of lightweight asymmetric cryptographic primitives in the IoT nodes deployed in smart-cities



#### **EVALUATION PLATFORMS**

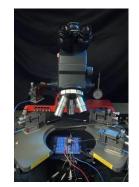
• More than 15 dedicated, home-made, test benches



Nano-Characterization Platform



Side Channel Platform



**Physical attacks Platform** 



J-TAG Platform



Fault injection Platform

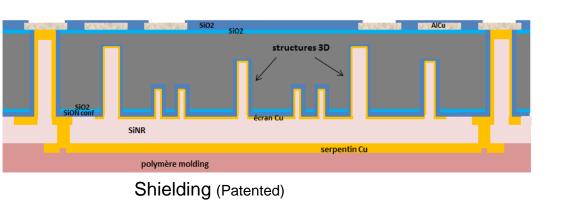


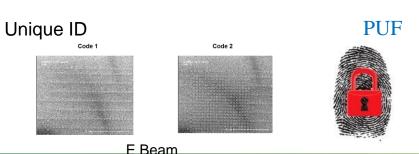
Software analysis Platform (LIST)

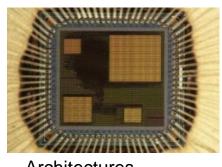


#### Challenges: Low resources

- Adapted cryptography (Stream cypher, ECC, ...)
- Reduce the Nb of counter-measures
- Choose low resources ones: from active to passive
- New protections

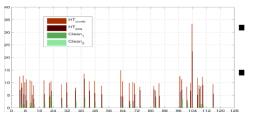






Architectures

- Dual rail encoding
- Masking
- Fault detection
- Sensors



Trojan / Clones detection Authenticity

25



## NFC

- Listening (more than 20m)
- Relay attacks

## **Smartphones**

• DPA (EM) on a Cryptolib

## **Biometrics**

• Fake fingers spoofing







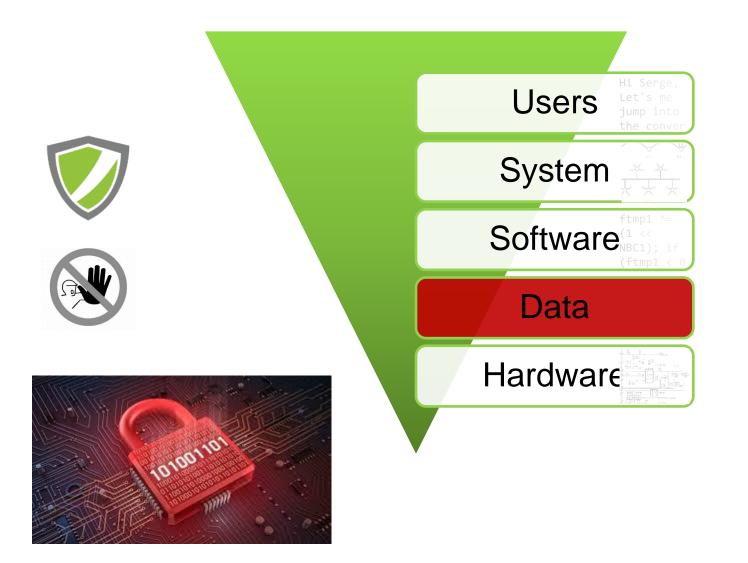










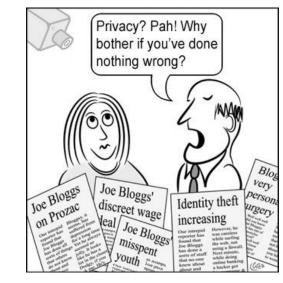




#### PRIVACY



- Many supports can tell so much
  - Obvious : e-ID / Internet / RFID / Localization
  - Less obvious: IoT / WSN / Wireless / Side-channel
  - Cross-channel tools: aggregation / data mining
- Raise partners awareness
- Provide protections
- Research activities
  - Anonymity
  - Untraceability
  - Unlinkability
  - Pseudonimization



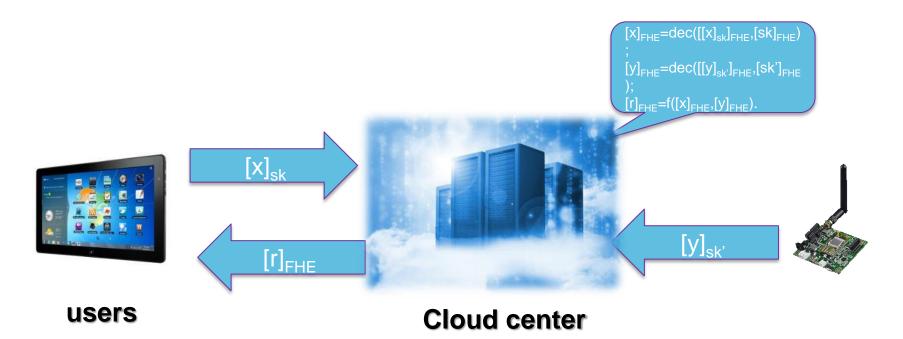
- Next generation e-ID documents
- Secure protocols for the IoT
- Contactless systems
- And generally embedded systems



#### **HOMOMORPHIC ENCRYPTION**



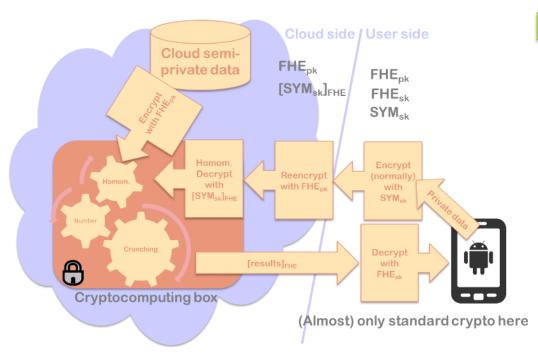
The cloud computers can process data &/or perform calculations from users and IoT sensor nodes without revealing the data







#### Perform blindly a medical diagnosis (Cardiovascular disease risk factors)



#### PERFORMANCES

- 3.3 secs for program execution on the server
- < 4 secs RTD towards servers.

#### SETUP

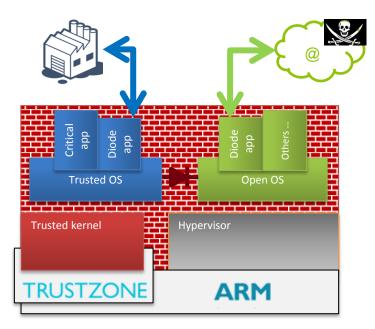
- The Android tablet sends the encrypted private user health data
- The server receives and homomorphically « transcripts »
- The server homomorphically executes the diagnostic algorithm and sends back the encrypted answer to the tablet.
- the tablet is the only party able to decrypt and thus interpret the server reply





#### SECURE EXECUTION ENVIRONMENTS ADVANCED CRYPTOGRAPHY





Protect the confidentiality and integrity of computations even in case of compromission

- HW/SW partitioning of a trusted functionality
- Spatial and temporal security by design of the execution environment
- Fully homomorphic encryption SDK for cloud computation scenarios

Atos THALES

list

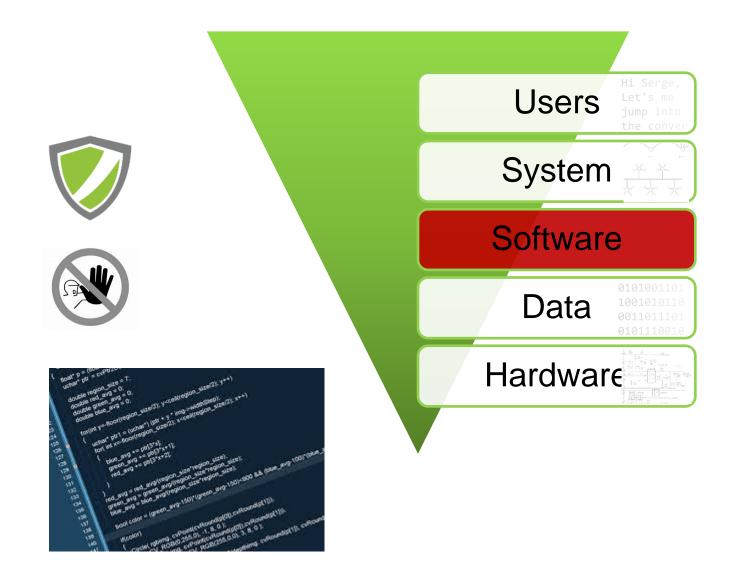
Ceatech

Feasible industrial implementations of homomorphic encrypted computations



Implementation of a high-reliability, high-performance operating system

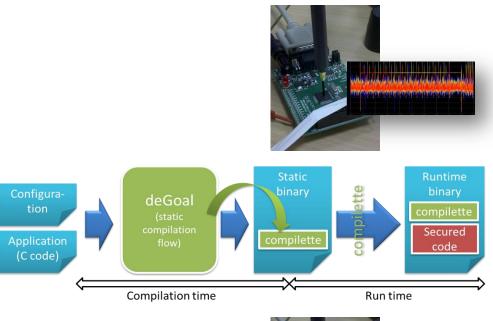


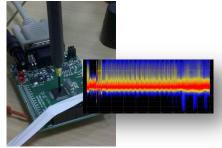




How to generate components that are protected against reverse engineering-based attacks?

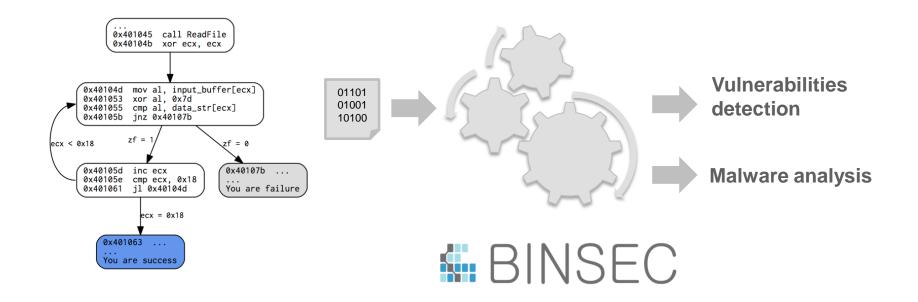
- Runtime code generation using code polymorphism techniques
- deGoal static compilation phase
- Compilette runtime generator
- fast code generation & tiny memory footprint







Perform flawless binary code analysis based on mathematical reasoning: simulation, static analysis and symbolic execution.





Pre-integration of an exhaustive malware analysis engine into an advanced security toolbox



Verify the source code in critical components for "advanced vulnerabilities"

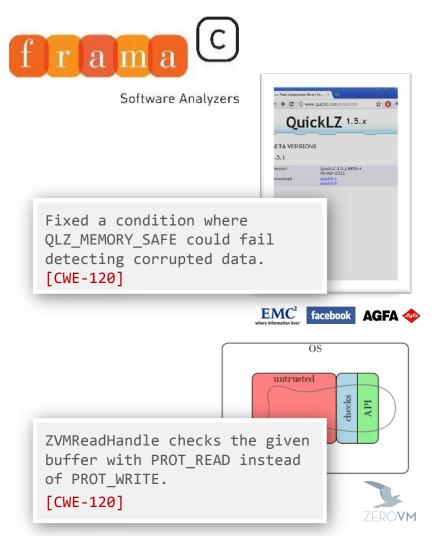
- Complete detection of the most common vulnerability classes
- Providing mathematical guarantees for a security perimeter
- Advanced verifications: API security policy checks, information flow analysis, runtime monitoring, ...



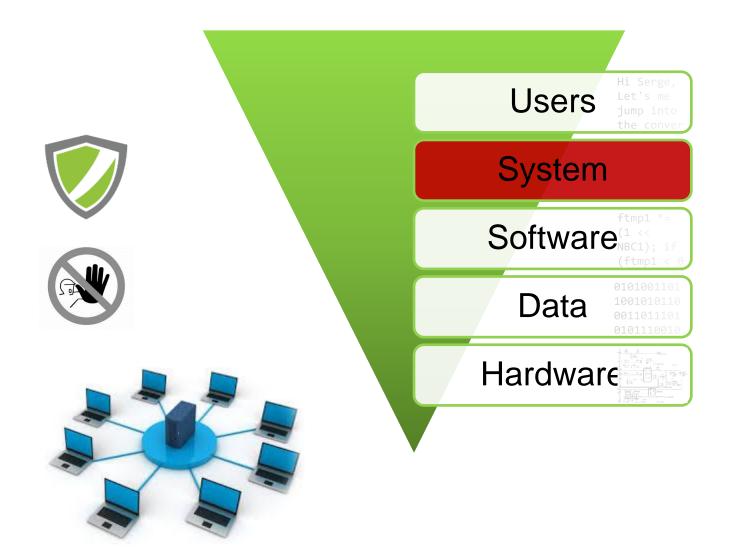
list

Ceatech

Software analysis tool reaches the highest "Ockham Criteria" for vulnerability detection





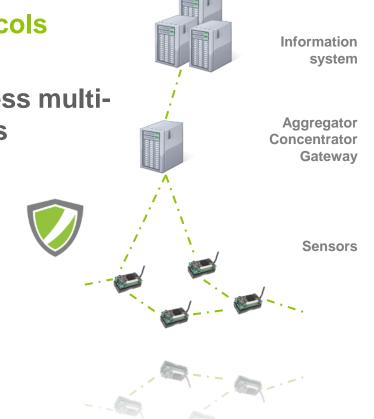




#### **INDUSTRIAL NETWORK PROTOCOL ANALYSIS**

What if we need high-integrity assessment of our network protocols?

- Analyze threats on industrial protocols
- Implement attacks on wired / wireless multiprotocol industrial sensor networks
- Tailored tools and testbeds
- Propose security solutions within certification constraints





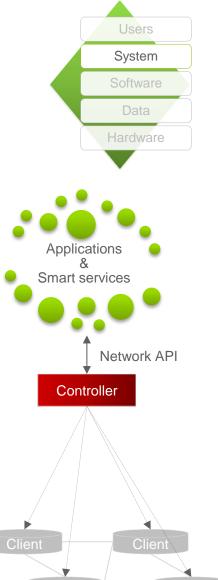
#### **COGNITIVE NETWORK SECURITY**

Reconfigure the network automatically in reaction to cyberattacks

- The NEON platform for Software-Defined Networking
  - Smart data routing
  - Fast infrastructure reconfiguration for new tasks
  - Fast deployment of network protocols and services
  - Intrusion detection & dynamic reconfiguration of security services
  - Mobile networking (5G)
- Adaptative resilience to threats from inside and outside the network
- Blueprint for Pan-European Resilient Critical
   Infrastructures based on LTE Communications



Countering attacks with SDN-based intrusion detection and network reconfiguration



Client

Client



## NEON → Trusted networks Software-Defined Networking

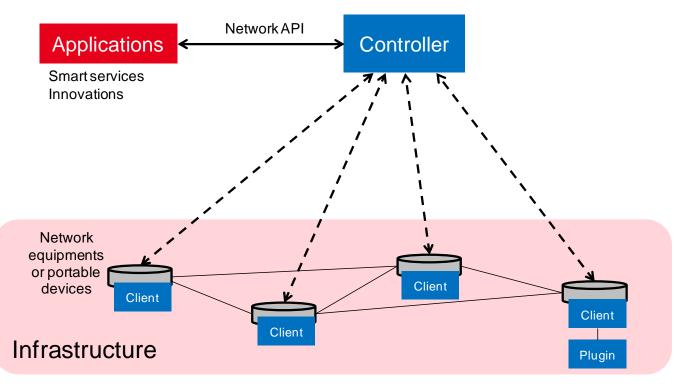
Software technology enabling smart management and control of network infrastructures / equipment / devices (NEON)

- Fast and easy control of equipment / devices, network interfaces, traffic, network resources and network services
- Interworking with OpenFlow protocol + additional Southbound protocol
- Applicable to linux-based equipment and end-terminals (e.g. Android devices)

# **Applications**

- Smart data routing
- Fast infrastructure reconfiguration for new tasks
- Fast deployment of network protocols and services
- Security: intrusion detection & dynamic reconfiguration of security services







#### **NETWORK SECURITY**

Can we ensure communications security in constrained networks?

- Lightweight + strong IP security protocols
  - Authentication & network access control
  - Dynamic key establishment
  - Secure software update
- Scalable distributed IDS
  - Lightweight data structures & footprint
  - Remote selection and configuration of monitoring nodes
- Multi-layer security for increased robustness against attacks





Security software running on lowpower platforms (e.g. IEEE 802.15.4 platform from Dresden Elektronik)





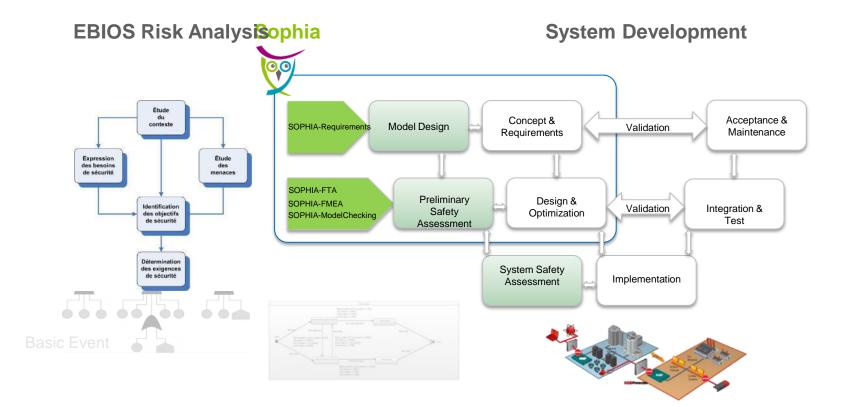
IDS software for 802.15.4 networks (e.g. Raspberry Pi platform)



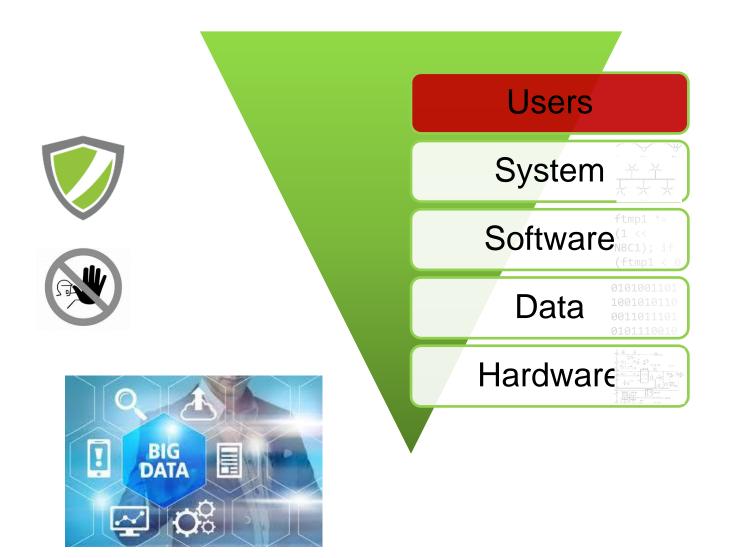




#### **AUTOMATED RISK ANALYSIS**









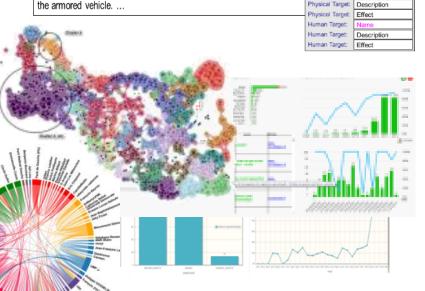
### THREAT INTELLIGENCE

#### Making sense of artefacts, communications, and interactions.

- **Data analysis** 
  - Pattern identification
  - Traffic analysis
  - Text and picture analysis
- Information search
  - Multimedia, multilingual
- **Visual analytics**



San Salvador, 19 Apr 89 (ACAN-EFE) -- [TEXT] Salvadoran President-elect Alfredo Cristiani condemned the terrorist killing of Attorney General Roberto Garcia Alvarado and accused the Farabundo Marti National Liberation Front (FMLN) of the crime. ... Garcia Alvarado, 56, was killed when a bomb placed ncident by urban guerrillas on his vehicle exploded as it came to a halt at an Incident: intersection in downtown San Salvador. ... Vice President-elect Francis Incident: Individual ID Pernetrator Merino said that when the attorney general's car stopped at a light on a Perpetrator street in downtown San Salvador, an individual placed a bomb on the ro Perpetrator the armored vehicle. ... Physical Target: Human Target



Location

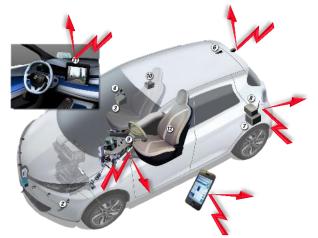
Org. Confidence



#### **FROM ANALYSIS TO SOLUTIONS**



## Characterization of the Threats



0 { if (#(Y->p + 1) != (t\_ if (ret != 0) goto cleanup:  $\label{eq:constraint} \begin{array}{l} -\infty_{-} \quad \operatorname{ext} = \sup_{k} \underline{\operatorname{grow}}(X_{k}): \quad \text{if } (\operatorname{ext} = \overline{\operatorname{ext}}) \underbrace{\operatorname{grow}}_{k}(\operatorname{void}) \\ \operatorname{sizes}(t_{k};\operatorname{uint})): \quad \operatorname{memp}((\operatorname{void}) \otimes X_{k}, \operatorname{void}) \\ \operatorname{sizes}(t_{k};\operatorname{uint})): \quad \operatorname{memp}((\operatorname{void}) \otimes X_{k}) \\ \operatorname{sizes}(t_{k};\operatorname{uint}) \\ \operatorname$ ret = mpi\_grow(X, i) : if (z < (t sint)0) X->s = -1; else X->s = 1 pi \*X, size\_t pos) { int \_\_retres: if (K->n \* (sizeof(t\_u)int goto return\_label: ] \_\_retres = (int) ((\*(X->p + pos / (sizeo seof(t\_u)int) << 3)) & 0x01): return\_label: return \_\_retres: }</pre> return\_label: return \_\_retres: ] r val) { int \_\_retres: int ret: size\_t pos, unsigned char val) {
 off = pos / (sizeof(t\_uint) <
 if ((int)val != 1) { \_\_retres = \_\_retres = ret: return\_labs ))) | ( (int)val << idx): cleanup:: ze\_t mpi\_lsb(mpi const \*X) { size\_t \_\_ trained int 0: i = (unsigned int 0: while (i < X->n) [
t\_uint) << 3) { if (((e0(C->p + 1) >> )) & 1) != 0) {
} j ++: count ++: } i ++: } \_\_retres = (unsigned j ↔: count ↔: ] i ↔: ] \_\_retres = (unsigned int) requires ¥valid\_read(0): assigns ¥result\_statei ass \_state ¥from\_statei \*/ size\_t mpi\_mmb(mpi const \*0) { = ₩<mark>chn = (sizm\_t)1</mark>: while (i) (sizm\_t)0] i ff (\*(C) -: ] j = sizeof(t\_uint) ≪ 3: while (j) (sizm\_t)0] { Wresult = -0x10: ensures Wold(0)->s = 1 V Wold assigns Wresult Wfrom \_state: assigns \*X Wfrom t mpi\_read\_string(mpi \*X, int radix, char const \*s) e\_t j; size\_t slen; size\_t n: t\_uint d: mpi T; 4: goto return\_label: ] else if (radix > 16) [ (size\_t) 1) / (sizeof (t\_uint) ret = mpi\_grow(X, n if (ret ret = mpi |set(X, (long)0) if (ret != 0) goto cleanup; i = slen; i = 

### Evaluation / Certification



Addrence do rapport de certification			
ANSSI-CC-2009/25	5		
Hom-do produkt			
Microcontrôleurs RISC 32-bits SAN	ISUNG S3FS9117		
\$3F\$91H / \$3F\$91V / \$3F\$93L av	ec SWP, Rév. 7		
Adlormos/varsion du produit			
Microcontrilleur : SJES91,J/ SJES91H/ SJES91W/ SJES931 - R6v.7			
Librairies logicielles : Test Rom code version 1.0, RSA library version 3.95, TRNG			
library version 1.0, Secure Roofleader	version 10		
Contomité à un profil de protection			
BSI-PP-0035			
Security IC Platform Protection Profile Vers	ion 1.0 June 2007		
Ottores dilivatuation of variation			
Critères Communs version	3.1 (R3)		
Weat deviation			
EAL 5 augmenté			
ALC DVS2 AVA VAN5			
Développeur			
Samsung Electronics Co	o. Ltd		
San#24 Nongsto-Ri, Giltering-Eup, Yongin-City, Gyronggi-Do, 449-711,			
République de Corée			
Converdible			
Samsung Electronics Co	o, Ltd		
San(24 Nonpeo-Ri, Giheung-Eug, Yongin-City, Greenggi-Do, 449-711,			
République de Cerrie			
Centra ditvakation			
CEA - LETI			
17 rue des martyrs, 38854 Greneble Colex 5, France			
Tel : +33 (954 38 78 40 87, mel : certia	etiitteadr		
Accords de reconnaissance applicadive			
CCRA	SOG-IS		
	Security Contract		
Le produit est reconnu au niveau EAL4.			

## Improvement of Security



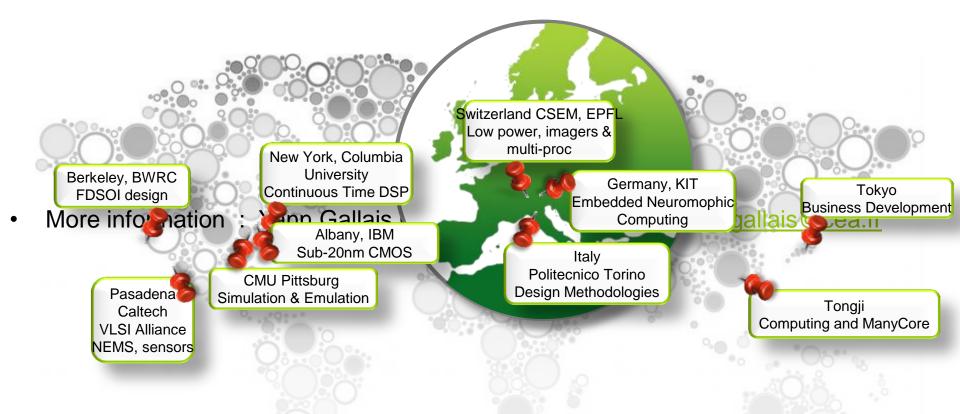




- At CEA Tech (LIST, LETI, DPACA), we provide tools for analysing the security issues pertaining cyber systems, whether at
  - System's level
  - Device level
  - Component level
- The difficulty & challenge is to build a securitycoherent approach through those different tools to ensure a coherent security chain.



#### **WORLDWIDE RESEARCH RELATIONSHIPS**



- More information : Yann Gallais
  - CEA-TECH Japan Office
    - Yann.gallais@cea.fr



