### I/O SCHEDULING IN HPC SYSTEMS

Guillaume Aupy





Project page: https://project.inria.fr/dash/

Collab. with JT Acquaviva (DDN), O Beaumont (Inria), L Eyraud-Dubois (Inria), E Jeannot (Inria), A Gainaru (Vanderbilt), V Le Fèvre (ENS Lyon), N Vidal (Inria)



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#### IO congestion in HPC systems:

- HPC applications are generating lots of data for PFS.
- ► Idea is to use a buffer when the I/O bandwidth is fully occupied
- ► The buffer can be emptied at a later time.

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Figure: Burst-buffers to absorb IO peaks (DDN material)

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### FINDING THE RIGHT SIZE

Overall, for various parameter we obtain something that looks like this:



Naive strategy:

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- If IO bandwidth avail.:  $\rightarrow$  use it
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### FINDING THE RIGHT SIZE

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![](_page_6_Figure_2.jpeg)

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"Online" scheduling:

- $\blacktriangleright$  When an application is ready to do I/O, it sends a message to an I/O scheduler;
- ► Based on the other applications running and a priority function, the I/O scheduler will give a **GO** or **NOGO** to the application.

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▶ If the application receives a **NOGO**, it pauses until a **GO** instruction.

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► Else, it performs I/O.

Gainaru, A., Benoit, Cappello, Robert, Snir, Scheduling HPC applications under I/O congestion, IPDPS'15

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![](_page_8_Figure_1.jpeg)

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![](_page_9_Figure_1.jpeg)

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![](_page_10_Figure_1.jpeg)

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![](_page_11_Figure_1.jpeg)

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![](_page_12_Figure_1.jpeg)

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![](_page_13_Figure_1.jpeg)

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![](_page_14_Figure_1.jpeg)

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![](_page_15_Figure_1.jpeg)

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![](_page_16_Figure_1.jpeg)

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![](_page_17_Figure_1.jpeg)

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![](_page_18_Figure_1.jpeg)

Approx 10% improvement in application performance with 5% gain in system performance on Intrepid.

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CAN WE DO BETTER?

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Assume we know applications I/O patterns:

▶ from historical data;

▶ ..

- ► because of periodic checkpointing;
- ► average filling speed of buffers
  - $\implies$  we know that every x min, y% of the buffer needs to be emptied;

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### Can we use this information and enforce efficient static schedules?

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Spoiler: it works very well (at least it seems promising)

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► Applications are already scheduled on the machines: not (yet) our job to do it;

#### A., Gainaru, Le Fèvre, Periodic I/O scheduling for super-computers, PMBS'17 ۰

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# We introduce Periodic Scheduling.

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A., Gainaru, Le Fèvre, Periodic I/O scheduling for super-computers, PMBS'17

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### PERIODIC SCHEDULES

![](_page_26_Figure_1.jpeg)

(a) Periodic schedule (phases)

![](_page_26_Figure_3.jpeg)

![](_page_27_Picture_0.jpeg)

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#### Time Schedule vs what Application 4 sees

![](_page_27_Figure_2.jpeg)

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- ► Distributed information
- ► Low complexity
- ▶ Minimum overhead

![](_page_28_Picture_0.jpeg)

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Time Schedule vs what Application 4 sees

![](_page_28_Figure_2.jpeg)

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- $\blacktriangleright$  Distributed information  $\checkmark$
- $\blacktriangleright$  Low complexity  $\checkmark$
- $\blacktriangleright$  Minimum overhead  $\checkmark$

# MODEL VALIDATION

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Setup:

- ▶ Applications from the literature, 10 sets with different contention.
- Comparison between simulations and a real machine (Jupiter @Mellanox: 640 cores, b = 0.01GB/s, B = 3GB/s).
- ▶ Instations with IOR benchmark (ideal world, no other communication than I/O transfers).

Algos:

- ▶ Periodic: our periodic algorithm;
- Online: the best performance of any online algorithm in Gainaru et al. IPDPS'15;
- Congestion: Current performance on the machine.

A., Gainaru, Le Fèvre, Periodic I/O scheduling for super-computers, PMBS'17

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RESULTS (EXPES)

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![](_page_30_Figure_1.jpeg)

The performance estimated by our model is accurate within 3.8% for periodic schedules and 2.3% for online schedules.

More results (simulations)

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![](_page_31_Figure_1.jpeg)

• We generate more sets of applications

 Simulate on instanciations of Intrepid and Mira.

# THE DASH PROJECT

- ► Understanding applications patterns
- Study of robustness: what if  $w_k$  and  $\operatorname{vol}_k^{io}$  are not exactly what they were supposed to be?
- ► Integrating non-periodic application
- ► Burst-buffers integration/modeling
- ► Coupling application scheduler to IO scheduler
- ► Evaluation on real applications

# Collaborations needed:

#### Three steps

- 1. I/O Modeling
- 2. Algorithm design
- 3. Evaluation and Integration

#### Application/system users:

- Data about I/O behaviors: patterns/periodicity, volume etc.
- ▶ Discussion about models of I/O scalability

#### From large-scale IO managers:

- Understanding traces, what kind of properties can we assume on the system?
- ▶ Discussion on implementation / integration
- ► Experiments

# Dash

DATA-AWARE SCHEDULING AT HIGHER SCALE

# Nicolas Vidal is starting his PhD this October. Talk to him S.

![](_page_34_Picture_3.jpeg)

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Papers, data, code: https://project.inria.fr/dash/

![](_page_34_Figure_6.jpeg)