

# Robust Synthesis for Real Time Systems<sup>☆</sup>

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## Abstract

Specification theories for real-time systems allow reasoning about interfaces and their implementation models, using a set of operators that includes satisfaction, refinement, logical and parallel composition. To make such theories applicable throughout the entire design process from an abstract specification to an implementation, we need to reason about the possibility to effectively implement the theoretical specifications on physical systems, despite their limited precision. In the literature, this implementation problem has been linked to the robustness problem that analyzes the consequences of introducing small perturbations into formal models.

We address this problem of robust implementations in timed specification theories. We first consider a fixed perturbation and study the robustness of timed specifications with respect to the operators of the theory. To this end we synthesize robust strategies in timed games. Finally, we consider the parametric robustness problem and propose a counter-example refinement heuristic.

*Keywords:* Stepwise refinement, Timed I/O automata, Timed games, Specification theory, Robustness

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## 1. Introduction

Component-based design is a software development paradigm well established in the software engineering industry. In component-based design, larger systems are built from smaller modules that depend on each other in well delimited ways described by interfaces. The use of explicit interfaces encourages creation of robust and reusable components.

Practice of component-based design is supported by a range of standardized middleware platforms such as CORBA [1], OSGi [2] or WSDL [3]. These technologies are usually not expressive enough to handle intricate correctness properties of

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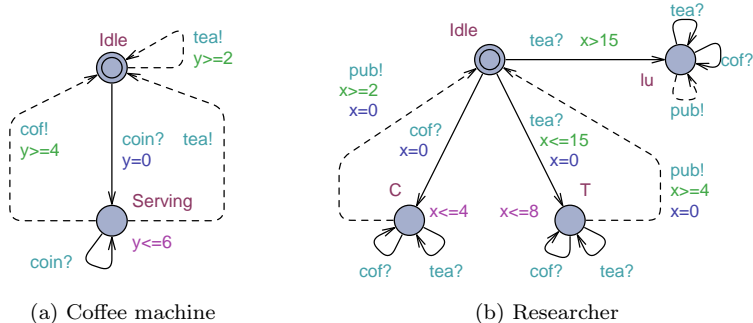


Figure 1: Timed specifications with timed I/O automata

safety-critical concurrent real-time software—a domain where component-based design would be particularly instrumental to address the strict legal requirements for certification [4]. To aid these needs researchers work on developing trustworthy rigorous methods for component-oriented design. In the field of concurrency verification this includes compositional design (specification theories, stepwise-refinement) and compositional model checking. Akin to algebraic specifications, *specification theories* provide a language for specifying component interfaces together with operators for combining them, such as parallel (structural) composition or conjunction (logical composition), along with algorithms for verification based on refinement checking.

For real-time systems, timed automata [5] are the classical specification language. Designs specified as timed automata are traditionally validated using model-checking against correctness properties expressed in a suitable timed temporal logic [6]. Mature modeling and model-checking tools exist, such as Uppaal [7], that implement this technique and have been applied to numerous industrial applications.

In [8], we have proposed a specification theory for real time systems based on timed automata. A specification theory uses refinement checking instead of model-checking to support compositionality of designs and proofs from ground up. We build on an input/output extension of timed automata model to specify both models *and properties*. The set of state transitions of the timed systems is partitioned between inputs, representing actions of the environment, and outputs that represent the behavior of the component. The theory is equipped with a game-based semantic. The two players, Input and Output, compete in order to achieve a winning objective—for instance safety or reachability. These semantics are used to define the operations of the theory, including satisfaction (can a specification be implemented), refinement (how two specifications compare), logical composition (superposition of two specifications), structural composition (combining smaller components into larger ones), and quotient (synthesizing a component in a large design).

Let us illustrate the main concepts with an example. Figure 1a displays a

specification of a coffee machine that receives an input `coin?` and outputs either coffee (`cof!`) or tea!. It can be composed with the specification of a researcher in Fig. 1b by synchronizing input with output labeled with the same channel (`cof` and `tea`). In Fig. 1b the researcher specifies that if `tea` arrives after 15 time units, she enters into an error state `lu`. We can say that if there exists an environment for these two specifications that avoids reaching this error state then the two specifications are compatible [9]. In this particular example, such an environment simply needs to provide `coin?` sufficiently often. In general deciding existence of safe environments is reduced to establishing whether there exists a winning strategy in the underlying timed safety game.

Besides compatibility checking, the theory of [8] is equipped with a consistency check to decide whether a specification can indeed be implemented. Unfortunately, this check does not take limitations and imprecision of the physical world into account. This is best explained with an example. Consider the refined specification of the coffee machine in Fig. 2. This machine first proposes to make a choice of a drink, then awaits a coin, and after receiving the payment it delivers the coffee. If the payment does not arrive within 6 time units, the machine aborts the drink selection and returns to the initial state, awaiting a new choice of a beverage. Already in this simple example it is quite hard to see that implementing a component satisfying this specification is not possible due to a subtle mistake. The two first steps of the machine are controlled by the environment, and not the system itself. Thus any implementation has to be able to accept the following behaviour: first `choice?` and then the `coin?` arriving precisely 6 time units after the choice. However then we arrive at the state (`Serving`,  $y = 6$ ) which requires that the coffee (`cof!`) must be delivered immediately, in zero time. No physical system would permit this, so we say that this state is not robustly consistent.

The above example can be fixed easily by adding another reset to clock  $y$ , when the `coin?` message is received. It is probably the intended behaviour of the specification that the serving should take 6 time units from the insertion of the coin, and not from the choice of the drink. After all the machine does not control how much time passes between the choice of the drink and the payment. An alternative simple fix is to allow the timeout `abort` transition to be taken earlier — for example after 4 time units. This would guarantee at least 2 time units for brewing the coffee. Despite both corrections being quite simple, it is clear that subtle timing mistakes like this one are very hard to spot. Finding such errors in specifications is even harder in larger designs as non-robust timing can emerge in the compositions of multiple specifications, as a result of combing behaviours that themselves are robust. These problems appear in compositions of independent components, which may appear spuriously compatible, but their

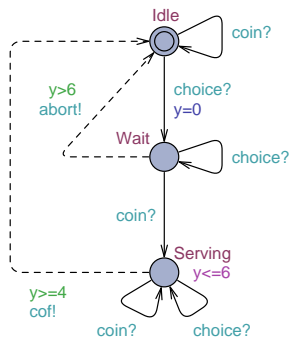


Figure 2: Non robust specification of a coffee machine

compatibility requires infinitely precise execution platform and (or) an infinitely fast environment.

The timing precision errors in specifications are not handled in any way in idealized interface theories such as [8, 10]. These and similar issues have led to a definition of the timing *robustness problem* that checks if a model can admit some timing perturbations while preserving a desired property. The robustness problem has been studied in various works for timed automata. Providing a solution to this problem in the setting of timed I/O specifications is the objective of this paper. Our contributions include:

- We propose a notion of implementation of a specification that is robust with respect to a given perturbation in the delay before an action. The concept of robust implementation is lifted to a robust satisfaction operation that takes variations of timed behaviors into account when checking whether the implementation matches the requirement of the specification.
- Classical compositional design operators are lifted to the robust setting. One of the remarkable features of this new theory is that this does not require to modify the definitions of the operators themselves and that all the good properties of a specification theory (including independent implementability) are maintained.
- We propose a consistency check for robust satisfaction. This new check relies on an extension of the classical timed I/O game to the robust setting. In [11], Chatterjee et al. show that problems on robust timed games can be reduced to classical problems on an extended timed game. We modify the original construction of [11] to take the duality of inputs and outputs into account. Then, we show how our new game can be used to decide consistency in a robust setting as well as to synthesize a robust implementation from a given specification.
- Finally, we present a technique that evaluates the greatest admissible perturbations for the robustness problems. We apply a counterexample abstraction refinement-like technique that analyzes parametrically the results of lost timed games in order to refine the value of the perturbation. This technique and the different constructions presented in the paper are implemented in prototype tool. We demonstrate its performances compared to a simple binary search technique for finding an optimal precision value.

To the best of our knowledge, this paper presents the first theory for stepwise refinement and specification of timed systems in a robust manner. While the presentation is restricted to the theory of [8], we believe that our methods work for any timed specifications. Our experience with industrial projects shows that such realistic design theories are of clear interest [12, 13, 14].

*Organization of the paper.* We proceed by summarizing the state of the art in Section 2 and introducing the background on Timed Specifications (Section 3). In Section 4 we introduce methods for solving robust time games that arise

in our specification theory. These methods are used in Sections 5 to reason about consistency, conjunction, parallel composition, in order to synthesize robust implementations of real time components. In Section 6 we developed a counterexample refinement technique to measure the imprecision allowed by the specifications. Finally in Section 7 we present a prototype that implements some of the functionalities of the tool ECDAR, extended with the robustness concepts presented in this paper. We demonstrate this tool on two experiments.

## 2. State of The Art

In the literature the robustness problem has been considered for timed automata using logical formulas as specifications (and neglecting compositional design operators). The robust semantics for timed automata with clock drifts has been introduced by Puri [15]. The problem has been linked to the implementation problem in [16], which introduced the first semantics that modeled the hardware on which the automaton is executed. In this work, the authors proposed a robust semantics of Timed Automata called AASAP semantics (for “Almost As Soon As Possible”), that enlarges the guards of an automaton by a delay  $\Delta$ . This work has been extended in [17] that proposes another robust semantics with both clock drifts and guard enlargement. Extending [15] they solve the robust safety problem, defined as the existence of a non-null value for the imprecision. They show that in terms of robust safety the semantics with clock drifts is just as expressive as the semantics with delay perturbation. We extend the work of [16, 17] by considering compositional design operators, stepwise-refinement, and reasoning about open systems (only closed system composition were considered so far).

We solve games for consistency and compatibility using a robust controller synthesis technique inspired by Chatterjee et al. [11], who provide synthesis techniques for robust strategies in games with parity objectives. Driven by the fact that consistency and compatibility are safety games, we restrict ourselves to safety objectives, but we extend [11] by allowing negative perturbation of delays.

Our paper is also similar to the works in [18, 19] that show how one can synthesize from any timed automaton an equivalent robust automaton. We also synthesize robust components, but we start from the specification and we apply a controller synthesis methods to the specification, rather than modifying an existing implementation.

Robustness is defined in [17] as the existence of a positive value for the imprecision of a timed automata. The papers shows that this problem is decidable, but it does not show how to synthesize the value. A bound on the value is computed in [20]. Finally a quantitative analysis is performed in [21] that computes the greatest admissible value for the perturbation, but the method is restricted to timed automata without nested loops. We propose an approximation technique that evaluates this value in our timed specifications context, with no major restrictions on syntax of the specifications.

There is presently no alternative specification theory for timed systems with support for robustness. A preliminary version of this paper has appeared in [22].

This version differs from the short version by including proofs of theorems, by adding two entirely new sections presenting parametric methods for robustness (Sections 6–7). The algorithms of [22] merely check whether a given specification is robust with respect to a given timing precision parameter. The new methods compute the maximum value of the precision for which the specification is robustly consistent (or two specifications are robustly compatible). We also add an experimental evaluation of the performance of the proposed methods.

### 3. Background on Timed I/O Specifications

We now recall the definition of Timed I/O specifications [8]. We use  $\mathbb{N}$  for the set of all natural numbers,  $\mathbb{R}$  for the set of all real numbers, and  $\mathbb{R}_{\geq 0}$  (resp.  $\mathbb{R}_{> 0}$ ) for the non-negative (resp. strictly positive) subset of  $\mathbb{R}$ . Rational numbers are denoted by  $\mathbb{Q}$ , and their subsets are denoted analogously. For  $x \in \mathbb{R}_{\geq 0}$ , let  $\lfloor x \rfloor$  denote the integer part of  $x$  and  $\langle x \rangle$  denote its fractional part. Given a function  $f$  on a domain  $D$  and a subset  $C$  of  $D$ , we denote by  $f|_C$  the restriction of  $f$  to  $C$ .

#### 3.1. Timed I/O Transitions Systems and Timed I/O Automata

In the framework of [8], specifications and their implementations are semantically represented by Timed I/O Transition Systems (TIOTS) that are nothing more than timed transition systems with input and output modalities on transitions. Later we shall see that input represents the behaviors of the environment in which a specification is used, while output represents behaviours of the component itself.

##### 3.1.1. Timed I/O Transitions Systems

**Definition 1** A Timed I/O Transition System is a tuple  $S = (St^S, s_0, \Sigma^S, \rightarrow^S)$ , where

- $St^S$  is an infinite set of states,
- $s_0 \in St^S$  is the initial state,
- $\Sigma^S = \Sigma_i^S \uplus \Sigma_o^S$  is a finite set of actions partitioned into inputs  $\Sigma_i^S$  and outputs  $\Sigma_o^S$ ,
- and  $\rightarrow^S: St^S \times (\Sigma^S \cup \mathbb{R}_{\geq 0}) \times St^S$  is a transition relation.

We write  $s \xrightarrow{a}^S s'$  when  $(s, a, s') \in \rightarrow^S$ , and use  $i?$ ,  $o!$  and  $d$  to range over inputs, outputs and  $\mathbb{R}_{\geq 0}$ , respectively.

In what follows, we assume that any TIOTS satisfies the following conditions:

- time determinism: whenever  $s \xrightarrow{d}^S s'$  and  $s \xrightarrow{d}^S s''$  then  $s' = s''$
- time reflexivity:  $s \xrightarrow{0}^S s$  for all  $s \in St^S$

- time additivity: for all  $s, s'' \in St^S$  and all  $d_1, d_2 \in \mathbb{R}_{\geq 0}$  we have  $s \xrightarrow{d_1+d_2}^S s''$  iff  $s \xrightarrow{d_1}^S s'$  and  $s' \xrightarrow{d_2}^S s''$  for  $s' \in St^S$

A run  $\rho$  of a TIOTS  $S$  from its state  $s_1$  is a sequence

$$s_1 \xrightarrow{a_1}^S s_2 \xrightarrow{a_2}^S \dots \xrightarrow{a_n}^S s_{n+1}$$

such that for all  $1 \leq i \leq n$ ,  $s_i \xrightarrow{a_i}^S s_{i+1}$  with  $a_i \in \Sigma^S \cup \mathbb{R}_{\geq 0}$ . We write  $\text{Runs}(s_1, S)$  for the set of runs of  $S$  starting in  $s_1$  and  $\text{Runs}(S)$  for  $\text{Runs}(s_0, S)$ . We write  $\text{States}(\rho)$  for the set of states reached in  $\rho$ , and if  $\rho$  is finite  $\text{last}(\rho)$  is the last state occurring in  $\rho$ . If silent actions (denoted  $\tau$ ) are used, they are hidden in the runs.

A TIOTS  $S$  is *deterministic* iff the action or delay fully determines the next state:  $\forall a \in \Sigma^S \cup \mathbb{R}_{\geq 0}$ , whenever  $s \xrightarrow{a}^S s'$  and  $s \xrightarrow{a}^S s''$ , then  $s' = s''$ . It is *input-enabled* iff there is an input transition for every input action in each of its states  $s \in St^S$ :  $\forall i? \in \Sigma_i^S. \exists s' \in St^S. s \xrightarrow{i?}^S s'$ .

A TIOTS is *output urgent* iff whenever an output transition is possible, no further delaying is allowed:

$$\forall s, s', s'' \in St^S \text{ if } s \xrightarrow{o!}^S s' \text{ and } s \xrightarrow{d}^S s'' \text{ then } d = 0$$

Output urgency captures predictability of timing of system's reactions. Finally, we say that a TIOTS  $S$  verifies the *independent progress* condition iff for all its states  $s$  either ( $\forall d \geq 0. s \xrightarrow{d}^S$ ) or ( $\exists d \in \mathbb{R}_{>0}. \exists o! \in \Sigma_o^S. s \xrightarrow{d}^S s'$  and  $s' \xrightarrow{o!}^S$ ) for some state  $s'$ . If  $S$  verifies independent progress, then it can always evolve using transitions it controls (delays and outputs), regardless whether the environment collaborates or not. This property will guarantee that the environment cannot block progress of time.

### 3.1.2. Timed I/O Automata

TIOTS are syntactically represented by *Timed I/O Automata (TIOA)*. Let  $Clk$  be a finite set of *clocks*. A *clock valuation* over  $Clk$  is a mapping  $Clk \mapsto \mathbb{R}_{\geq 0}$  (thus  $\mathbb{R}_{\geq 0}^{Clk}$ ). Given a valuation  $u$  and  $d \in \mathbb{R}_{\geq 0}$ , we write  $u+d$  for the valuation in which for each clock  $x \in Clk$  we have  $(u+d)(x) = u(x) + d$ . For  $\lambda \subseteq Clk$ , we write  $u[\lambda]$  for a valuation agreeing with  $u$  on clocks in  $Clk \setminus \lambda$ , and giving 0 for clocks in  $\lambda$ .

Let  $\Phi(Clk)$  denote all *clock constraints*  $\varphi$  generated by the grammar  $\varphi ::= x \prec k \mid x - y \prec k \mid \varphi \wedge \varphi$ , where  $k \in \mathbb{Q}$ ,  $x, y \in Clk$  and  $\prec \in \{<, \leq, >, \geq\}$ . For constraint  $\varphi \in \Phi(Clk)$  and  $u \in \mathbb{R}_{\geq 0}^{Clk}$ , we write  $u \models \varphi$  if  $u$  satisfies  $\varphi$ . If  $Z \subseteq \mathbb{R}_{\geq 0}^{Clk}$ , we write  $Z \models \varphi$  if  $u \models \varphi$  for all  $u \in Z$ . We write  $\llbracket \varphi \rrbracket$  to denote the set of valuations  $\{u \in \mathbb{R}_{\geq 0}^{Clk} \mid u \models \varphi\}$ . A subset  $Z \subseteq \mathbb{R}_{\geq 0}^{Clk}$  is a *zone* if  $Z = \llbracket \varphi \rrbracket$  for some  $\varphi \in \Phi(Clk)$ . Let  $Clk' \subset Clk$  and  $Z \subseteq \mathbb{R}_{\geq 0}^{Clk}$  be a zone. We define the projection of  $Z$  on the subset of clocks  $Clk'$  as  $Z|_{Clk'} = \{u' \in \mathbb{R}_{\geq 0}^{Clk'} \mid \exists u \in Z. \forall x \in Clk'. u'(x) = u(x)\}$ .

**Definition 2** A Timed I/O Automaton is a tuple  $\mathcal{A} = (Loc, q_0, Clk, E, Act, Inv)$ , where

- $Loc$  is a finite set of locations,
- $q_0 \in Loc$  is the initial location,
- $Clk$  is a finite set of clocks,
- $E \subseteq Loc \times Act \times \Phi(Clk) \times 2^{Clk} \times Loc$  is a set of edges,
- $Act = Act_i \uplus Act_o$  is a finite set of actions, partitioned into inputs ( $Act_i$ ) and outputs ( $Act_o$ ),
- $Inv : Loc \mapsto \Phi(Clk)$  is a set of location invariants.

Without loss of generality we assume that invariants of a location are always included in the guards of the edges that are incident with the location. We also assume that guards are satisfiable (for any guard  $\varphi$  the set  $\llbracket \varphi \rrbracket$  is non-empty).

A universal location, denoted  $l_u$ , in a TIOA accepts every input and can produce every output at any time. The location  $l_u$  will be used to model an unpredictable behavior of a component.

**Example 1** Figure 1b depicts an example of a TIOA that admits two input actions *cof?* and *tea?*, and one output action *pub!*. Transitions are labeled with an action, a guard and a set of reset clocks. Input transitions are drawn with plain arrows, while output transitions are drawn with dashed arrows. Location are labeled with a name and an invariant constraint. This example contains a universal location ( $l_u$ ).

The semantics of a TIOA  $\mathcal{A} = (Loc, q_0, Clk, E, Act, Inv)$  is a TIOTS  $\llbracket \mathcal{A} \rrbracket_{sem} = (Loc \times \mathbb{R}_{\geq 0}^{Clk}, (q_0, \mathbf{0}), Act, \rightarrow)$ , where  $\mathbf{0}$  is a constant function mapping all clocks to zero, and  $\rightarrow$  is the largest transition relation generated by the following rules:

- Each edge  $(q, a, \varphi, \lambda, q') \in E$  gives rise to  $(q, u) \xrightarrow{a} (q', u')$  for each clock valuation  $u \in \mathbb{R}_{\geq 0}^{Clk}$  such that  $u \models \varphi$  and  $u' = u[\lambda]$  and  $u' \models Inv(q')$ .
- Each location  $q \in Loc$  with a valuation  $u \in \mathbb{R}_{\geq 0}^{Clk}$  gives rise to a transition  $(q, u) \xrightarrow{d} (q, u + d)$  for each delay  $d \in \mathbb{R}_{\geq 0}$  such that  $u + d \models Inv(q)$ .

**Example 2** In the example of Fig. 1b a possible run starting from initial location *Idle* is

$$(Idle, (0)) \xrightarrow{cof?} (C, (0)) \xrightarrow{2.6} (C, (2.6)) \xrightarrow{pub!} (Idle, (0))$$

Let  $X$  be a set of states in  $\llbracket \mathcal{A} \rrbracket_{sem}$ . For  $a \in Act$  the  $a$ -successors and  $a$ -predecessors of  $X$  are defined respectively by:

$$\begin{aligned} Post_a(X) &= \{(q', u') \mid \exists (q, u) \in X. (q, u) \xrightarrow{a} (q', u')\} \\ Pred_a(X) &= \{(q, u) \mid \exists (q', u') \in X. (q, u) \xrightarrow{a} (q', u')\} \end{aligned}$$

The timed successors and predecessors of  $X$  are respectively defined by:

$$\begin{aligned} X \nearrow &= \{(q, u + d) \mid (q, u) \in X, d \in \mathbb{R}_{\geq 0}\} \\ X \searrow &= \{(q, u - d) \mid (q, u) \in X, d \in \mathbb{R}_{\geq 0}\} \end{aligned}$$



The *safe* timed predecessors of  $X$  with respect to a set of unsafe states  $Y$  are the set of timed predecessors of  $X$  such that the states of  $Y$  are avoided along the path:

$$\text{Pred}_t(X, Y) = \{(q, u) \mid \exists d \in \mathbb{R}_{\geq 0}. (q, u) \xrightarrow{d} (q, u + d) \text{ and } (q, u + d) \in X \\ \text{and } \forall d' \in [0, d]. (q, u + d') \notin Y\}$$

These operations can be implemented symbolically on zones using Difference Bound Matrices (DBMs) [23].

### 3.1.3. Symbolic abstractions

Since TIOTs are infinite size they cannot be directly manipulated by computations. Usually *symbolic representations*, such as *region graphs* [5] or *zone graphs*, are used as data structures that finitely represent semantics of TIOAs. A *symbolic state* is a pair  $(q, Z)$  that combines all concrete states  $(q, u)$  such that  $u \in Z$ , where  $q \in \text{Loc}$  and  $Z \subseteq \mathbb{R}_{\geq 0}^{\text{Clk}}$ . Usually symbolic states are formed combining locations with special kinds of sets of valuations: *regions* and *zones*. Recall that zones are sets expressed by clock constraints in TIOAs. We now define regions.

Let  $M$  be the integer constant with the greatest absolute value among constants appearing in guards of a TIOA<sup>1</sup>. A *clock region* is an equivalence class of the relation  $\sim$  on clock valuations such that  $u \sim v$  iff the following conditions hold:

- $\forall x \in \text{Clk}$ , either  $\lfloor u(x) \rfloor = \lfloor v(x) \rfloor$ , or  $u(x) > M$  and  $v(x) > M$ ,
- $\forall x, y \in \text{Clk}$ ,  $\forall k \in [-M, M]$ ,  $u(x) - u(y) \leq k$  iff  $v(x) - v(y) \leq k$ ,
- $\forall x \in \text{Clk}$  if  $u(x) \leq M$  then  $\langle u(x) \rangle = 0$  iff  $\langle v(x) \rangle = 0$ ,

We write  $r \nearrow$  for the region that is the unique direct time successor of region  $r$ , if such exists. For a clock valuation  $u$ , we write  $[u]$  to denote the unique region containing  $u$ .

The *region graph* of a TIOA  $\mathcal{A}$  is a triple  $\mathcal{G}_{\mathcal{A}} = (\mathcal{R}_{\mathcal{A}}, r_0, \rightarrow)$ , where  $\mathcal{R}_{\mathcal{A}} = \{(q, [u]) \mid (q, u) \in \text{St}^{\llbracket \mathcal{A} \rrbracket^{\text{sem}}}\}$  is the set of symbolic states,  $r_0 = (q_0, [\mathbf{0}])$  is the initial symbolic state, and  $\rightarrow \subseteq \mathcal{R}_{\mathcal{A}} \times (\text{Act} \cup \{\tau\}) \times \mathcal{R}_{\mathcal{A}}$ , such that  $(q, r) \xrightarrow{\tau} (q, r \nearrow)$  iff  $r \nearrow \models \text{Inv}(q)$ , and  $(q, r) \xrightarrow{a} (q', r')$  iff  $(q, u) \xrightarrow{a} (q', u')$  for some  $u \in r$  and  $u' \in r'$ .

The *zone graph*  $\mathcal{G}'_{\mathcal{A}} = (\mathcal{Z}_{\mathcal{A}}, X_0, \rightarrow)$  is defined analogously, but using zones instead of regions. It provides a coarser abstraction, in which only discrete transitions are observed. There,  $\mathcal{Z}_{\mathcal{A}}$  is the set of reachable symbolic states:  $(q, Z) \in \mathcal{Z}_{\mathcal{A}}$  if  $Z$  is a zone of  $\mathbb{R}_{\geq 0}^{\text{Clk}}$ . The initial symbolic state is defined by  $X_0 = \{(q_0, \mathbf{0} \nearrow \cap \llbracket \text{Inv}(q_0) \rrbracket)\}$ . For action  $a \in \text{Act}$  there is an edge  $(q, Z) \xrightarrow{a} (q', Z')$  iff  $(q, a, \varphi, \lambda, q') \in E$  with  $Z' = ((Z \cap \llbracket \varphi \rrbracket) [\lambda]) \nearrow \cap \llbracket \text{Inv}(q') \rrbracket$ .

<sup>1</sup>The region graph of an automaton with rational constants can be built by scaling all constants of the automaton to work only with integers.

**Example 3** The TIOA in Figure 1b is a specification of a researcher. It accepts either coffee (cof) or tea in order to produce publications (pub). If tea is served after a too long period the researcher falls into an error state, represented by the universal state  $lu$ .

An implementation of this specification is presented in Figure 3. It is output urgent since it produces **pub** exactly 3 time units after receiving **cof** and 6 time units after receiving **tea**. The location **Blocked** is an implementation of the universal location that never produced **pub**.

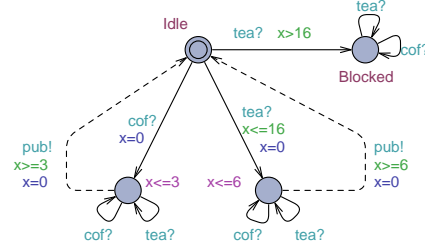


Figure 3: Implementation for a researcher

### 3.2. Basics of the Timed Specification Theory

In [8], timed specifications and implementations are both represented by TIOAs satisfying additional conditions:

**Definition 3** A specification  $\mathcal{S}$  is a TIOA whose semantics  $\llbracket \mathcal{S} \rrbracket_{\text{sem}}$  is deterministic and input-enabled. An implementation  $\mathcal{I}$  is a specification whose semantics  $\llbracket \mathcal{I} \rrbracket_{\text{sem}}$  additionally verifies the output urgency and the independent progress conditions.

In specification theories, a *refinement* relation plays a central role. It allows to compare specifications, and to relate implementations to specifications. In [8], as well as in [9, 24, 25], refinement is defined in the style of alternating (timed) simulation:

**Definition 4 (Refinement)** An alternating timed simulation between TIOAs  $S = (St^S, s_0, \Sigma^S, \rightarrow^S)$  and  $T = (St^T, t_0, \Sigma^T, \rightarrow^T)$  is a relation  $R \subseteq St^S \times St^T$  such that  $(s_0, t_0) \in R$  and for every  $(s, t) \in R$

- If  $t \xrightarrow{i?}^T t'$  for some  $t' \in St^T$ , then  $s \xrightarrow{i?}^S s'$  and  $(s', t') \in R$  for some  $s' \in St^S$
- If  $s \xrightarrow{o!}^S s'$  for some  $s' \in St^S$ , then  $t \xrightarrow{o!}^T t'$  and  $(s', t') \in R$  for some  $t' \in St^T$
- If  $s \xrightarrow{d}^S s'$  for  $d \in \mathbb{R}_{\geq 0}$ , then  $t \xrightarrow{d}^T t'$  and  $(s', t') \in R$  for some  $t' \in St^T$

We write  $S \leq T$  if there exists an alternating simulation between  $S$  and  $T$ . For two TIOAs  $\mathcal{S}$  and  $\mathcal{T}$ , we say that  $\mathcal{S}$  refines  $\mathcal{T}$ , written  $\mathcal{S} \leq \mathcal{T}$ , iff  $\llbracket \mathcal{S} \rrbracket_{\text{sem}} \leq \llbracket \mathcal{T} \rrbracket_{\text{sem}}$ .

**Definition 5 (Satisfaction)** An implementation  $\mathcal{I}$  satisfies a specification  $\mathcal{S}$ , denoted  $\mathcal{I} \text{ sat } \mathcal{S}$ , if and only if  $\llbracket \mathcal{I} \rrbracket_{\text{sem}} \leq \llbracket \mathcal{S} \rrbracket_{\text{sem}}$ .

We write  $\llbracket \mathcal{S} \rrbracket_{\text{mod}}$  for the set of all implementations of  $\mathcal{S}$ :

$$\llbracket \mathcal{S} \rrbracket_{\text{mod}} = \{ \mathcal{I} \mid \mathcal{I} \text{ sat } \mathcal{S} \text{ and } \mathcal{I} \text{ is an implementation} \}$$

The reader might find it surprising that in a robust specification theory we refrain from adjusting the refinement to account for imprecision of implementations when comparing specifications. Our basic assumption is that specifications are precise mathematical objects that are not susceptible to imprecision of execution. In contrary, implementations can behave imprecisely when executed, so in Section 4 we will introduce an extension of Def. 5 that takes this into account. It is a fortunate property of Def. 4 that we do not need to modify it in order to reason about robust implementations (Property 3 in Sect. 4).

In [8], we have reduced refinement checking to finding winning strategies in timed games. In the reminder of this section, we recall the definition of such games and show how they can be used to check consistency. Timed games also underly other operations such as conjunction, composition, and quotient [8], which will be illustrated in Sect. 5.

### 3.3. Timed Games for Timed I/O Specifications

TIOAs are interpreted as two-player real-time games between the *output player* (the component) and the *input player* (the environment). The input player plays with actions in  $Act_i$  and the output player plays with actions in  $Act_o$ . A strategy for a player is a function that defines her move at any given time (either delaying or playing a controllable action). A strategy is called *memoryless* if the next move depends solely on the current state. We only consider memoryless strategies, as these suffice for safety games [26]. For simplicity, we only define strategies for the output player (i.e. output is the verifier). Definitions for the input player are obtained symmetrically.

**Definition 6** A *memoryless* strategy  $f_o$  for the output player on the TIOA  $\mathcal{A}$  is a partial function  $St^{\llbracket \mathcal{A} \rrbracket_{\text{sem}}} \mapsto Act_o \cup \{\text{delay}\}$ , such that

- Whenever  $f_o(s) \in Act_o$  then  $s \xrightarrow{f_o(s)} s'$  for some  $s'$ .
- Whenever  $f_o(s) = \text{delay}$  then  $s \xrightarrow{d} s''$  for some  $d > 0$  and state  $s''$ , and  $f_o(s'') = \text{delay}$ .

The game proceeds as a concurrent game between the two player, each proposing its own strategy. The restricted behavior of the game defines the *outcome* of the strategies.

**Definition 7** Let  $\mathcal{A}$  be a TIOA,  $f_o$  and  $f_i$  be two strategies over  $\mathcal{A}$  for the output and input player, respectively, and  $s$  be a state of  $\llbracket \mathcal{A} \rrbracket_{\text{sem}}$ .  $\text{Outcome}(s, f_o, f_i)$  is the subset of  $\text{Runs}(s, \llbracket \mathcal{A} \rrbracket_{\text{sem}})$  defined inductively by:

- $s \in \text{Outcome}(s, f_o, f_i)$ ,
- if  $\rho \in \text{Outcome}(s, f_o, f_i)$ , then  $\rho' = \rho \xrightarrow{a} s' \in \text{Outcome}(s, f_o, f_i)$  if  $\rho' \in \text{Runs}(s, \llbracket \mathcal{A} \rrbracket_{\text{sem}})$  and one the following conditions hold:
  1.  $a \in Act_o$  and  $f_o(\text{last}(\rho)) = a$ ,
  2.  $a \in Act_i$  and  $f_i(\text{last}(\rho)) = a$ ,

3.  $a \in \mathbb{R}_{\geq 0}$  and  $\forall d \in [0, a[. \exists s''. \text{last}(\rho) \xrightarrow{d} s''$   
and  $\forall k \in \{o, i\} f_k(s'') = \text{delay}$ .

- $\rho \in \text{Outcome}(s, f_o, f_i)$  if  $\rho$  is infinite and all its finite prefixes are in  $\text{Outcome}(s, f_o, f_i)$ .

A *winning condition* for a player in the TIOA  $\mathcal{A}$  is a subset of  $\text{Runs}(\llbracket \mathcal{A} \rrbracket_{\text{sem}})$ . In safety games the winning condition is to avoid a set **Bad** of “bad” states. Formally, the winning condition for output is  $W^o(\text{Bad}) = \{\rho \in \text{Runs}(\llbracket \mathcal{A} \rrbracket_{\text{sem}}) \mid \text{States}(\rho) \cap \text{Bad} = \emptyset\}$ . A strategy  $f_o$  is a *winning strategy* from state  $s$  if and only if, for all strategy  $f_i$  of input,  $\text{Outcome}_o(s, f_o, f_i) \subseteq W^o(\text{Bad})$ . On the contrary, a strategy  $f_i$  for input is a *spoiling strategy* of  $f_o$  if and only if  $\text{Outcome}(s, f_o, f_i) \not\subseteq W^o(\text{Bad})$ . A state  $s$  is winning for output if there exists a winning strategy from  $s$ . The game  $(\mathcal{A}, W^o(\text{Bad}))$  is winning if and only if the initial state is winning. Solving this game is decidable [27, 23, 8]. We only consider safety games in this paper, and without loss of generality we assume these “bad” states correspond to a set of entirely “bad” locations.

*Strategies in Timed Games as Operators on Timed Specifications:* . We sketch how timed games can be used to establish consistency of a timed specifications.

An *immediate error* occurs in a state of a specification if the specification disallows progress of time and output transitions in a given state—such a specification will break if the environment does not send an input. For a specification  $\mathcal{S}$  we define the set of immediate error states  $\text{err}^{\mathcal{S}} \subseteq \text{St}^{\llbracket \mathcal{S} \rrbracket_{\text{sem}}}$  as:

$$\text{err}^{\mathcal{S}} = \{s \mid (\exists d. s \not\xrightarrow{d}) \text{ and } \forall d \forall o! \forall s'. s \xrightarrow{d} s' \text{ implies } s' \not\xrightarrow{o!}\}$$

It follows that no immediate error states can occur in implementations, since they verify independent progress. In [8] we show that  $\mathcal{S}$  is consistent iff there exists a winning strategy for output in the safety game  $(\mathcal{S}, W^o(\text{err}^{\mathcal{S}}))$ . Moreover, the maximum consistent part of  $\mathcal{S}$  corresponds to the maximum winning strategy for output in this game.

Conjunction of two specifications is found as a maximal strategy for output in a safety game on the product state space to avoid immediate errors. Similarly, optimistic parallel composition of two specifications is computed as a maximum strategy for input in a safety game over the product state space; and a quotient of two specifications is found as a maximum strategy for output in another safety game. Optimistic composition means that two specifications are compatible if there exists at least one environment, in which they can avoid error states. Details can be found in [8].

#### 4. Robust Timed I/O Specifications

We now define a robust extension of our specification theory. An essential requirement for an implementation is to be realizable on a physical hardware, but this requires admitting small imprecisions characteristic for physical components (computer hardware, sensors and actuators). The requirement of realizability

has already been linked to the robustness problem in [16] in the context of model checking. In specification theories the small deficiencies of hardware can be reflected in a strengthened satisfaction relation, which introduces small perturbations to the timing of implementation actions, before they are checked against the requirements of a specification. This ensures that the implementation satisfies the specification even if its behavior is perturbed.

We first formalize the concept of perturbation. Let constraint  $\varphi \in \Phi(X)$  be a guard over the set of clocks  $X$ . The *enlarged guard*  $\lceil \varphi \rceil_\Delta$  is constructed according to the following rules:

- Any term  $x \prec k$  of  $\varphi$  with  $\prec \in \{<, \leq\}$  is replaced by  $x \prec k + \Delta$
- Any term  $x \succ k$  of  $\varphi$  with  $\succ \in \{>, \geq\}$  is replaced by  $x \succ k - \Delta$

Similarly, the *restricted guard*  $\lfloor \varphi \rfloor_\Delta$  is using the two following rules:

- Any term  $x \prec k$  of  $\varphi$  with  $\prec \in \{<, \leq\}$  is replaced by  $x \prec k - \Delta$
- Any term  $x \succ k$  of  $\varphi$  with  $\succ \in \{>, \geq\}$  is replaced by  $x \succ k + \Delta$ .

Notice that for a clock valuation  $u$  and a guard  $\varphi$ , we have that  $u \models \varphi$  implies  $u \models \lceil \varphi \rceil_\Delta$ , and  $u \models \lfloor \varphi \rfloor_\Delta$  implies  $u \models \varphi$ , and  $\lceil \lceil \varphi \rceil_\Delta \rceil_\Delta = \lceil \lfloor \varphi \rfloor_\Delta \rfloor_\Delta = \varphi$ .

#### 4.1. Perturbed Implementation and Robust Timed I/O Specifications.

We lift the perturbation to implementation TIOAs. Given a jitter  $\Delta$ , the perturbation means a  $\Delta$ -enlargement of invariants and of output edge guards. Guards on the input edges are restricted by  $\Delta$ :

**Definition 8** For an implementation  $\mathcal{I} = (Loc, q_0, Clk, E, Act, Inv)$  and  $\Delta \in \mathbb{Q}_{>0}$ , the  $\Delta$ -perturbation of  $\mathcal{I}$  is the TIOA  $\mathcal{I}_\Delta = (Loc \cup \{l_u\}, q_0, Clk, E', Act, Inv')$ , such that:

- Every edge  $(q, o!, \varphi, \lambda, q') \in E$  is replaced by  $(q, o!, \lceil \varphi \rceil_\Delta, \lambda, q') \in E'$ ,
- Every edge  $(q, i?, \varphi, \lambda, q') \in E$  is replaced by  $(q, i?, \lfloor \varphi \rfloor_\Delta, \lambda, q') \in E'$ ,
- $\forall q \in Loc. Inv'(q) = \lceil Inv(q) \rceil_\Delta$ ,
- $\forall q \in Loc. \forall i? \in Act_i$  there exists an edge  $(q, i?, \varphi_u, \emptyset, l_u) \in E'$  with

$$\varphi_u = \neg \left( \bigvee_{(q, i?, \varphi, \lambda, q') \in E} \lceil \varphi \rceil_\Delta \right)$$

$\mathcal{I}_\Delta$  is not necessarily action deterministic, as output guards are enlarged. However it is input-enabled, since by construction (last case in previous definition), any input not accepted after restricting input guards is redirected to the universal location  $l_u$ . Also  $\mathcal{I}_0$  equals  $\mathcal{I}$ .

In essence, we weaken the constraints on output edges, and strengthen the constraints on input edges. This is consistent with the game semantics

of specifications: perturbation makes the game harder to win for the verifier. Since the gaps created by strengthening input guards are closed by edges to the universal location, the implementation becomes less predictable. If an input arrives close to the deadline, the environment cannot be certain if it will be handled precisely as specified. Enlargement of output guards has a similar effect. The environment of the specification has to be ready that outputs will arrive slightly after the deadlines.

Such considerations are out of place in classical robustness theories for model checking, but are crucial when moving to models, where input and output transitions are distinguished. For example, in [16] the authors propose a robust semantics for timed automata. Their *maximal progress assumption* is equivalent to the output urgency condition of our implementations. However, in [16] both input and output guards are increased, which is suitable for the one-player setting, but incompatible with the contravariant nature of two-player games. Such enlargement would not be monotonic with respect to the alternating refinement (Def. 4), while the perturbation of Def. 8 is monotonic.

We are now ready to define our notion of robust satisfaction:

**Definition 9** *An implementation  $\mathcal{I}$  robustly satisfies a specification  $\mathcal{S}$  for a given delay  $\Delta \in \mathbb{Q}_{\geq 0}$ , denoted  $\mathcal{I} \text{ sat}_{\Delta} \mathcal{S}$ , if and only if  $\mathcal{I}_{\Delta} \leq \mathcal{S}$ .*

*We write  $\llbracket \mathcal{S} \rrbracket_{\text{mod}}^{\Delta}$  for the set of all  $\Delta$ -robust implementations of  $\mathcal{S}$ , such that*

$$\llbracket \mathcal{S} \rrbracket_{\text{mod}}^{\Delta} = \{\mathcal{I} \mid \mathcal{I} \text{ sat}_{\Delta} \mathcal{S} \wedge \mathcal{I} \text{ is an implementation}\}$$

**Property 1 (Monotonicity)** *Let  $\mathcal{I}$  be an implementation and  $0 \leq \Delta_1 < \Delta_2$ . Then:*

$$\mathcal{I} \leq \mathcal{I}_{\Delta_1} \leq \mathcal{I}_{\Delta_2}$$

**Proof 1 (Property 1)** *First, observe that for any clock valuation  $u$  and guard  $\varphi$ , if  $u \models \lceil \varphi \rceil_{\Delta_1}$  then  $u \models \lceil \varphi \rceil_{\Delta_2}$ , and conversely if  $u \models \lfloor \varphi \rfloor_{\Delta_2}$  then  $u \models \lfloor \varphi \rfloor_{\Delta_1}$ . We now check the refinement between  $\llbracket \mathcal{I}_{\Delta_1} \rrbracket_{\text{sem}}$  and  $\llbracket \mathcal{I}_{\Delta_2} \rrbracket_{\text{sem}}$ . Let  $R = \{(s_1, s_2) \in St^{\llbracket \mathcal{I}_{\Delta_1} \rrbracket_{\text{sem}}} \times St^{\llbracket \mathcal{I}_{\Delta_2} \rrbracket_{\text{sem}}} \mid s_1 = (q, u) = s_2\}$  be a candidate alternating simulation relation. We prove by co-induction that  $R$  satisfies Def. 4. Consider any state  $(q, u)$  such that  $(q, u)R(q, u)$ .*

1. *If  $(q, u) \xrightarrow{d}^{I_1} (q, u + d)$  for some  $d \in \mathbb{R}_{\geq 0}$ , then  $(q, u) \xrightarrow{d}^{I_2} (q, u + d)$ , since  $u + d \models \lceil \text{Inv}(q) \rceil_{\Delta_1} \Rightarrow u + d \models \lceil \text{Inv}(q) \rceil_{\Delta_2}$ .*
2. *If  $(q, u) \xrightarrow{o!}^{I_1} (q', u')$ , then  $(q, u) \xrightarrow{o!}^{I_2} (q', u')$ , since  $u \models \lceil \varphi \rceil_{\Delta_1} \Rightarrow u \models \lceil \varphi \rceil_{\Delta_2}$  (where  $\varphi$  is the guard of the edge that fires  $o!$ ) and we assumed the invariant of  $q'$  is included in the guard.*
3. *If  $(q, u) \xrightarrow{i?}^{I_2} (q', u')$  then  $(q, u) \xrightarrow{i?}^{I_1} (q', u')$ , since  $u \models \lfloor \varphi \rfloor_{\Delta_2} \Rightarrow u \models \lfloor \varphi \rfloor_{\Delta_1}$  (where  $\varphi$  is the guard of the edge that fires  $i?$ ) and we assumed the invariant of  $q'$  is included in the guard.*

*Clearly, both transition systems  $\llbracket \mathcal{I}_{\Delta_1} \rrbracket_{\text{sem}}$  and  $\llbracket \mathcal{I}_{\Delta_2} \rrbracket_{\text{sem}}$  share the same initial state  $s_0$  and  $s_0 R s_0$ , which concludes the proof. The argument that  $\mathcal{I} \leq \mathcal{I}_{\Delta_1}$  proceeds similarly with the same witness relation  $R$ .  $\square$*

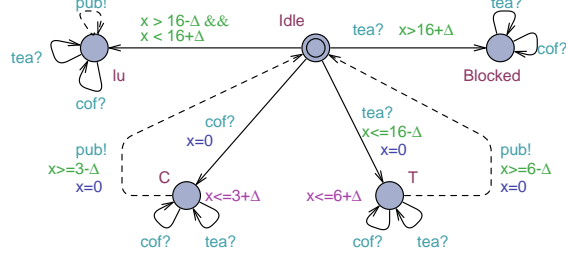


Figure 4:  $\Delta$ -perturbation of the researcher implementation.

In addition, we obtain these properties by transitivity of alternating simulation:

**Property 2** *Let  $\mathcal{S}$  be a specification and  $\Delta_1 \leq \Delta_2$ . Then:*

$$\llbracket \mathcal{S} \rrbracket_{\text{mod}}^{\Delta_2} \subseteq \llbracket \mathcal{S} \rrbracket_{\text{mod}}^{\Delta_1} \subseteq \llbracket \mathcal{S} \rrbracket_{\text{mod}}$$

**Property 3** *Let  $\mathcal{S}$  and  $\mathcal{T}$  be specifications and  $0 \leq \Delta$ . Then:*

$$\mathcal{S} \leq \mathcal{T} \implies \llbracket \mathcal{S} \rrbracket_{\text{mod}}^{\Delta} \subseteq \llbracket \mathcal{T} \rrbracket_{\text{mod}}^{\Delta}$$

The definition of robust satisfaction naturally induces a notion of robust consistency (implementability):

**Definition 10** *Let  $\mathcal{S}$  be a specification and  $\Delta \in \mathbb{Q}_{>0}$ , then  $\mathcal{S}$  is  $\Delta$ -robust consistent if there exists an implementation  $\mathcal{I}$  such that  $\mathcal{I} \text{ sat}_{\Delta} \mathcal{S}$ .*

Like in the non-robust case, deciding consistency is reducible to solving games. But now, we will need to make the games aware of the robustness conditions. In the rest of this section, we propose a definition for such games. Then, in Section 5 we show how they can be used to perform classical operations on specifications.

**Example 4** *Figure 4 presents the  $\Delta$ -perturbation of the researcher implementation presented in Fig. 3. This implementation robustly satisfies the specification of Fig. 1b for any  $\Delta \in ]0, 1]$ . However, for  $\Delta = 2$  the following run is possible in the perturbed implementation:*

$$(\text{Idle}, (0)) \xrightarrow{\text{cof?}} (\text{C}, (0)) \xrightarrow{5} (\text{C}, (5))$$

*but it cannot be matched by the specification because it exceeds the invariant of the location corresponding to C in Fig. 3.*

#### 4.2. Robust Timed Games for Timed I/O Specifications.

As we have seen timed specifications are interpreted as timed games. Solving games is used to analyze and synthesize real-time components. Now that we add imprecision to models, we need a notion of suitable games that can be used to synthesize robust components. Therefore we extend timed games with  $\Delta$ -perturbations, and study the synthesis of robust timed strategies.

De Alfaro et al. show [26] that timed games can be solved using region strategies, where the players only need to remember the sequence of locations and clock regions, instead of the sequence of states used in Definition 6. Consequently timed games can be solved through symbolic computations performed on symbolic graphs (either the region graph or the zone graph) using for instance the algorithm presented in [23]. The following definition formalizes the notion of a *symbolic strategies* which can be represented using symbolic states only:

**Definition 11** *A symbolic strategy  $F$  for the output player is a function  $Z \mapsto Act_o \cup \{\text{delay}\}$ , where  $Z$  is a set of symbolic states, such that whenever  $F((q, Z)) \in Act_o$  then for each  $u \in Z$  we have  $(q, u) \xrightarrow{F((q, Z))} (q', u')$  for some  $(q', u')$ . A symbolic strategy for the input player is defined analogously.*

A symbolic strategy  $F$  corresponds to the set of (non-symbolic explicit) strategies  $f$  such that whenever  $F((q, Z)) = a$  then  $f((q, u)) = a$  for some  $u \in Z$ .

*Syntactic outcomes.* The following construction represents the outcome of applying a symbolic strategy to a TIOA as another timed automaton. It decorates a region graph with clocks, guards and invariants. We exploit the region graph construction in the definition, but any stable partitioning of the state-space could serve this purpose, and would be more efficient in practice.

**Definition 12** *Let  $\mathcal{A} = (Loc, q_0, Clk, E, Act, Inv)$  be a TIOA and  $F$  a symbolic strategy over  $\mathcal{A}$  for output. The TIOA  $\mathcal{A}_F = (\mathcal{R}_{\mathcal{A}}, (q_0, r_0), Clk, \widehat{E}, Act \cup \{\tau\}, \widehat{Inv})$  representing the outcome of applying  $F$  to  $\mathcal{A}$  is built by decorating the region graph  $\mathcal{G}_{\mathcal{A}} = (\mathcal{R}_{\mathcal{A}}, \rightarrow^G)$  of  $\mathcal{A}$ . For each region  $(q, r)$ , the incident edges and the invariant are defined as follows:*

- If  $(q, r) \xrightarrow{\tau}^G (q, r\swarrow)$ , then  $((q, r), \tau, r\swarrow, \emptyset, (q, r\swarrow)) \in \widehat{E}$ ,
- If  $(q, r) \xrightarrow{i?}^G (q', r')$ , then for each edge  $(q, i?, \varphi, \lambda, q') \in E$ ,  $((q, r), i?, \varphi, \lambda, (q', r')) \in \widehat{E}$ ,
- If  $F((q, r)) = \text{delay}$  then  $\widehat{Inv}(q, r) = Inv(q) \wedge (r \vee r\swarrow)$ ,
- If  $F((q, r)) = o!$ , then  $\widehat{Inv}(q, r) = r$ , and if  $(q, r) \xrightarrow{o!}^G (q', r')$ , then for each edge  $(q, o!, \varphi, \lambda, q') \in E$ ,  $((q, r), o!, \varphi, \lambda, (q', r')) \in \widehat{E}$ .

In a robust timed game we seek strategies that remain winning after perturbation by a delay  $\Delta$ . The perturbation is defined on the syntactic outcome of the strategy, by enlarging the guards for the actions of the verifier, so that each



actions of the verifier can happen within  $\Delta$  time of what the strategy originally prescribes. We write  $\lceil \mathcal{A} \rceil_{\Delta}^{\circ}$  (respectively  $\lceil \mathcal{A} \rceil_{\Delta}^i$ ) for the TIOA where the guards of the output (resp. input) player and the invariants have been enlarged by  $\Delta$ —so every guard  $\varphi$  has been replaced by  $\lceil \varphi \rceil_{\Delta}$  and every invariant  $\gamma$  by  $\lceil \gamma \rceil_{\Delta}$ .

**Definition 13** *For a timed game  $(\mathcal{A}, W^{\circ}(\mathbf{Bad}))$ , a symbolic strategy  $F$  for output is  $\Delta$ -robust winning if it is winning when the moves of output are perturbed, i.e.*

$$\text{Runs}(\llbracket \lceil \mathcal{A}_F \rceil_{\Delta}^{\circ} \rrbracket_{\text{sem}}) \subseteq W^{\circ}(\mathbf{Bad})$$

In the rest of this section we describe a technique to find these robust strategies by modifying the original game automaton.

### 4.3. Robust game automaton

Robust timed games for a bounded delay can be reduced to classical timed games by a syntactic transformation of the game automaton [11]. Below in Def. 14, we propose an extended version of the construction presented in [11]. We admit both positive and negative perturbations of the player moves. In the original construction of [11] only delayed executions of actions were treated, but premature execution of communication may also lead to a safety violation in a specification theory, so we have to account for them. Then we show, in Theorem 1, how this construction can be used to find robust strategies as defined in Def. 13.

Let  $(\mathcal{A}, W^{\circ}(\mathbf{Bad}))$  be a timed game, where  $\mathcal{A} = (Loc, q_0, Clk, E, Act, Inv)$  and  $\mathbf{Bad} \in Loc$ . We assume that all the constants in  $\mathcal{A}$  are integers and we consider a perturbation  $\Delta \in \mathbb{N}$ .

**Definition 14** *The robust game automaton  $\mathcal{A}_{\text{rob}}^{\Delta} = (\widetilde{Loc}, q_0, Clk \cup \{y\}, \widetilde{E}, \widetilde{Act}, \widetilde{Inv})$  is constructed from  $\mathcal{A}$ , with an additional clock  $y$ , input actions  $\widetilde{Act}_i = Act_i \cup Act_o$ , and output actions  $\widetilde{Act}_o = \{\tau_{o!} \mid o! \in Act_o\}$ , according to the following rules:*

- For each location  $q \in Loc$ , then  $q \in \widetilde{Loc}$ , and for each edge  $e = (q, o!, \varphi, \lambda, q') \in E$ , two locations  $q_e^{\alpha}$  and  $q_e^{\beta}$  are added in  $\widetilde{Loc}$ . The invariant of  $q$  is unchanged; the invariants of  $q_e^{\alpha}$  and  $q_e^{\beta}$  are both  $y \leq \Delta$ .
- Each edge  $e' = (q, i?, \varphi, \lambda, q') \in E$  gives rise to the following edges in  $\widetilde{E}$ :  $(q, i?, \varphi, \lambda, q')$ ,  $(q_e^{\alpha}, i?, \varphi, \lambda, q')$  and  $(q_e^{\beta}, i?, \varphi, \lambda, q')$ .
- Each edge  $e = (q, o!, \varphi, \lambda, q') \in E$  gives rise to the following edges in  $\widetilde{E}$ :  $(q, \tau_{o!}, \varphi, \{y\}, q_e^{\alpha})$ ,  $(q_e^{\alpha}, \tau_{o!}, \{y = \Delta\}, \{y\}, q_e^{\beta})$ ,  $(q_e^{\alpha}, o!, \varphi, \lambda, q')$ ,  $(q_e^{\beta}, o!, \varphi, \lambda, q')$ ,  $(q_e^{\alpha}, o!, \neg\varphi, \emptyset, \mathbf{Bad})$  and  $(q_e^{\beta}, o!, \neg\varphi, \emptyset, \mathbf{Bad})$

Technically speaking, since in a TIOA transitions guards must be convex, the last two transitions may be split into several copies, one for each convex guard in  $\neg\varphi$ .

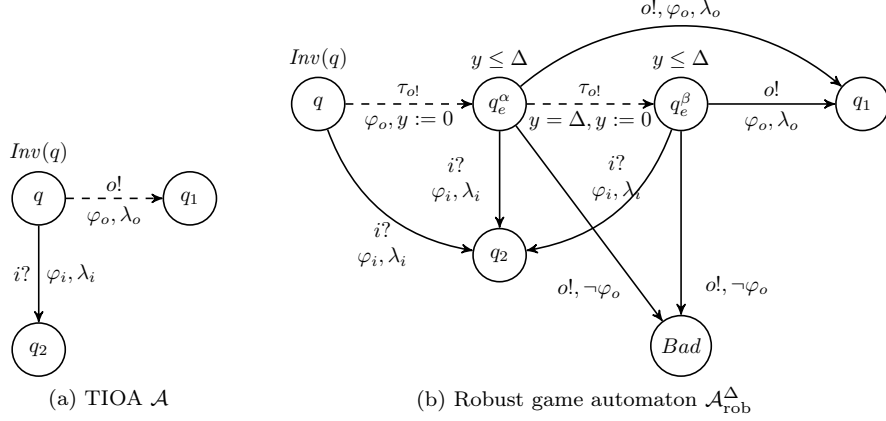


Figure 5: Illustration of the construction of the robust game automaton  $\mathcal{A}_{\text{rob}}^{\Delta}$ , from an automaton  $\mathcal{A}$ , with one location  $q$  and incident edges  $e = (q, o!, \varphi_o, \lambda_o, q_1)$  and  $(q, i?, \varphi_i, \lambda_i, q_2)$ .

The construction is illustrated in Fig. 5. Intuitively, whenever the output player wants to fire a transition induced by an edge  $(q, o!, \varphi_o, \lambda_o, q_1)$  in the original automaton, from a state  $(q, u)$ , after elapsing  $d$  time units, in the robust automaton the input player is allowed to perturb the timing of this action. Consider the following traces on the robust game automaton that explain the construction.

1. Output proposes to play action  $o!$  after a delay  $d$  with the following sequence of transitions:

$$(q, u) \xrightarrow{d-\Delta} (q, u + d - \Delta) \xrightarrow{\tau_{o!}} (q_e^{\alpha}, u + d - \Delta) \xrightarrow{\Delta} (q_e^{\alpha}, u + d) \xrightarrow{\tau_{o!}} (q_e^{\beta}, u + d)$$

Note that this forbid output to play any action with a reaction time smaller than  $\Delta$ . More precisely, any strategy for output found in the robust game automaton  $\mathcal{A}_{\text{rob}}^{\Delta}$  will effectively correspond to a non-zero strategy in  $\mathcal{A}$ .

2. Input can perturb this move with  $d' \leq \Delta$ , such that action  $o!$  is performed with either a smaller delay:

$$(q^{\alpha}, u + d - \Delta) \xrightarrow{d'} (q^{\alpha}, u + d - \Delta + d') \xrightarrow{o!} (q_1, u + d - \Delta + d')$$

or a greater delay:

$$(q^{\beta}, u + d) \xrightarrow{d'} (q^{\beta}, u + d + d') \xrightarrow{o!} (q_1, u + d + d')$$

3. In locations  $q, q^{\alpha}$  and  $q^{\beta}$ , the original input edge  $(q, i?, \varphi_i, \lambda_i, q_1)$  may still be fired. So while the execution of the output  $o!$  is delayed control can be intercepted by and arriving input.

4. If output reaches a state  $(q^\alpha, u)$  or  $(q^\beta, u)$ , with  $u \not\models \varphi_o$ , then input has a winning strategy with one of the following moves:  $(q^\alpha, u) \xrightarrow{o!} (\mathbf{Bad}, u)$  or  $(q^\beta, u) \xrightarrow{o!} (\mathbf{Bad}, u)$ , that denote the late firing of action  $o!$ .

Let  $F : \mathcal{R}_{\mathcal{A}_{\text{rob}}^\Delta} \mapsto \widetilde{\text{Act}}_o \cup \text{delay}$  be a winning symbolic strategy for output in the robust game  $(\mathcal{A}_{\text{rob}}^\Delta, W^\circ(\mathbf{Bad}))$ . We construct a strategy  $F_{\text{rob}} : \mathcal{R}_{\mathcal{A}} \mapsto \text{Act}_o \cup \text{delay}$  for the game  $(\mathcal{A}, W^\circ(\mathbf{Bad}))$  in the following manner.  $\forall (q, r) \in \mathcal{R}_{\mathcal{A}}$ ,

- $F_{\text{rob}}((q, r)) = o!$  if there exists an edge  $e \in E$  and a region  $(q_e^\alpha, \tilde{r}) \in \mathcal{R}_{\mathcal{A}_{\text{rob}}^\Delta}$ , such that  $r = \tilde{r}|_{\text{Cik}}$  and  $F((q_e^\alpha, \tilde{r})) = \tau_{o!}$ .
- Otherwise  $F_{\text{rob}}((q, r)) = \text{delay}$ .

**Theorem 1** *The robust game automaton is a sound construction to solve robust timed games in the following sense: if  $F$  is a winning strategy for output in the game  $(\mathcal{A}_{\text{rob}}^\Delta, W^\circ(\mathbf{Bad}))$ , then  $F_{\text{rob}}$  (constructed above) is a  $\Delta$ -robust winning strategy for output in the game  $(\mathcal{A}, W^\circ(\mathbf{Bad}))$ .*

**Proof 2 (Theorem 1)** *We consider the automaton  $\mathcal{A}_{F_{\text{rob}}}$  representing the outcome of applying  $F_{\text{rob}}$  to  $\mathcal{A}$ . We must prove that  $\text{Runs}(\llbracket \llbracket \mathcal{A}_{F_{\text{rob}}} \rrbracket_\Delta^o \rrbracket_{\text{sem}}) \subseteq W^\circ(\mathbf{Bad})$ , where  $W^\circ(\mathbf{Bad}) = \{\rho \in \text{Runs}(\llbracket \llbracket \mathcal{A} \rrbracket_{\text{sem}}) \mid \text{States}(\rho) \cap \mathbf{Bad} = \emptyset\}$ .*

*First, we map each run  $\hat{\rho}$  of  $\llbracket \llbracket \mathcal{A}_{F_{\text{rob}}} \rrbracket_\Delta^o \rrbracket_{\text{sem}}$  to a run  $\rho$  of  $\mathcal{A}$ . We use an induction on the length of the runs. We assume that the property holds for runs of length  $i$ : if  $\hat{\rho}_i$  is a run of  $\text{Runs}(\llbracket \llbracket \mathcal{A}_{F_{\text{rob}}} \rrbracket_\Delta^o \rrbracket_{\text{sem}})$  such that  $\text{last}(\hat{\rho}_i) = ((q_i, r_i), u_i)$ , then there exists a run  $\rho_i$  in  $\text{Runs}(\llbracket \llbracket \mathcal{A} \rrbracket_{\text{sem}})$ , such that  $\text{last}(\rho_i) = (q_i, u_i)$ .*

*Let  $\hat{\rho}_{i+1} = \hat{\rho}_i \xrightarrow{a} ((q_{i+1}, r_{i+1}), u_{i+1})$ . We prove the inductive step, splitting into cases:*

1. *If  $a \in \text{Act}_i$ , then there exists an edge  $\hat{e} = ((q_i, r_i), a, \varphi, \lambda, (q_{i+1}, r_{i+1}))$  in  $\mathcal{A}_{F_{\text{rob}}}$ . By construction, there also exists an edge  $e = (q_i, a, \varphi, \lambda, q_{i+1})$  in  $\mathcal{A}$ . Since  $\hat{e}$  is fireable,  $u_i \models \varphi$ , and therefore  $e$  and  $\tilde{e}$  are also fireable. So  $\rho_{i+1} = \rho_i \xrightarrow{a} (q_{i+1}, u_{i+1})$  is a run of  $\mathcal{A}$ .*
2. *If  $a \in \text{Act}_o$ , then there exists  $\hat{e} = ((q_i, r_i), a, \varphi, \lambda, (q_{i+1}, r_{i+1}))$  in  $\mathcal{A}_{F_{\text{rob}}}$  and by construction,  $e = (q_i, a, \varphi, \lambda, q_{i+1})$  in  $\mathcal{A}$ . Since  $\hat{e}$  is fireable,  $u_i \models \lceil \varphi \rceil_\Delta$  and  $u_i \models \lceil r_i \rceil_\Delta$  that is the enlarged invariant of  $(q_i, r_i)$ . By construction also  $F_{\text{rob}}((q_i, r_i)) = a$ , which implies that there exists  $\tilde{r}_i$  such that  $r_i = \tilde{r}_i|_{\text{Cik}}$  and  $F((q_i^\alpha, \tilde{r}_i)) = a$ . Since  $u_i \models \lceil r_i \rceil_\Delta$ ,  $\exists \delta \in [-\Delta, \Delta]. u_i + \delta \models r_i$ . Let  $u_i = (u_{i1}, u_{i2}, \dots, u_{in})$ , then  $u_i + \delta = (u_{i1} + \delta, u_{i2} + \delta, \dots, u_{in} + \delta)$ . By definition of the projection this implies that  $\tilde{u}_{i\delta} = (u_{i1} + \delta, u_{i2} + \delta, \dots, u_{in} + \delta, \Delta) \in \tilde{r}_i$ . Now in the automaton  $\mathcal{A}_{\text{rob}}^\Delta$ ,  $F$  is a winning strategy, which implies that  $\forall \delta' \in [-\Delta, \Delta]. \tilde{u}_{i\delta} + \delta' \models \varphi$  (otherwise input as a spoiling strategy). This proves that  $u_i \models \varphi$  and therefore that  $e$  is fireable. So  $\rho_{i+1} = \rho_i \xrightarrow{a} (q_{i+1}, u_{i+1})$  is a run of  $\mathcal{A}$ .*
3. *If  $a \in \mathbb{R}_{\geq 0}$ , either the strategy  $F_{\text{rob}}$  prescribes that output can delay infinitely in  $((q_i, r_i), \hat{u}_i)$ . This implies that  $\text{Inv}(q_i)$  is unbounded, and that proves immediately that  $\rho_{i+1} \in \text{Runs}(\llbracket \llbracket \mathcal{A} \rrbracket_{\text{sem}})$ .*

Otherwise output has a strategy that eventually performs an action. This is represented by the following sequence of edges in  $\mathcal{A}_{F_{\text{rob}}}$ :  $(q_i, r_i) \xrightarrow{\tau} (q_i, r_{i1}) \xrightarrow{\tau} (q_i, r_{i2}) \dots (q_i, r_{in}) \xrightarrow{o_1} (q'_i, r'_i)$ . We consider that  $a$  is the maximum delay firable from  $\hat{\rho}_i$ , thus  $\hat{\rho}_{i+1} = \hat{\rho}_i \xrightarrow{a_1} ((q_i, r_{i1}), u_i + a_1) \xrightarrow{\tau} ((q_i, r_{i2}), u_i + a_1) \xrightarrow{a_2} ((q_i, r_{i2}), u_i + a_1 + a_2) \xrightarrow{\tau} \dots ((q_i, r_{in}), u_i + a) \xrightarrow{o_1} ((q'_i, r'_i), u'_i)$ , such that  $a = \sum_{j=1}^n a_j$ . Then  $u_i + a \models [r_{in}]_{\Delta}$ . As in the previous case we can prove that  $u_i + a \models \varphi$ , and we assume that  $\varphi$  contains the location invariant. This proves that  $\rho_{i+1} \in \text{Runs}(\llbracket \mathcal{A} \rrbracket_{\text{sem}})$ .

We now prove that all the states in  $\llbracket \mathcal{A}_{F_{\text{rob}}} \rrbracket_{\Delta}^{\circ}$  are safe, by mapping the runs of  $\mathcal{A}_{F_{\text{rob}}}$  with the ones of  $\mathcal{A}_{\text{rob}}^{\Delta}$ . We use another induction on the length on the run. Let assume that  $\hat{\rho}_i \in \text{Runs}(\llbracket \llbracket \mathcal{A}_{F_{\text{rob}}} \rrbracket_{\Delta}^{\circ} \rrbracket_{\text{sem}})$  is mapped to a run  $\tilde{\rho}_i \in \text{Runs}(\llbracket \mathcal{A}_{\text{rob}}^{\Delta} \rrbracket_{\text{sem}})$ , such that if  $\text{last}(\hat{\rho}_i) = ((q_i, r_i), u_i)$ , then  $\text{last}(\tilde{\rho}_i) = (q_i, \tilde{u}_i)$ ,  $u_i = \tilde{u}_i \upharpoonright_{\text{Clock}}$ , and  $(q_i, \tilde{u}_i)$  is a winning state for the strategy  $F$ .

1. Either in  $((q_i, r_i), u_i)$  the strategy  $F_{\text{rob}}$  allows delaying infinitely, and consequently the strategy  $F$  allows delaying infinitely in  $\mathcal{A}_{\text{rob}}^{\Delta}$  without reaching an unsafe state. For any delay  $d \in \mathbb{R}_{\geq 0}$ , all the input actions  $i$ ? firable from a state  $((q_i, r'_i), u_i + d)$  are also firable in  $(q_i, \tilde{u}_i + d)$  since the guards are the same. Therefore,  $((q_i, r'_i), u_i + d) \xrightarrow{i?} ((q_{i+1}, r_{i+1}), u_{i+1})$ , and  $(q_i, \tilde{u}_i + d) \xrightarrow{i?} (q_{i+1}, \tilde{u}_{i+1})$ . Additionally,  $(q_{i+1}, \tilde{u}_{i+1})$  is also a winning state, since it is an outcome of the strategy  $F$ , and  $\tilde{u}_{i+1} = u_{i+1}$ , since the same clocks are reset. This proves the induction hypothesis
2. Otherwise, output has a strategy that eventually performs an action  $o!$  after a delay  $a$ . Let  $\hat{\rho}_{i+1} = \hat{\rho}_i \xrightarrow{a} ((q_i, r'_i), u_i + a) \xrightarrow{o_1} ((q_{i+1}, r_{i+1}), u_{i+1})$  (concealing  $\tau$  transitions). This implies that there exists an edge  $e \in \mathcal{A}$  that fires  $o!$  in  $q_{i+1}$ , the following edges exists in  $\mathcal{A}_{\text{rob}}^{\Delta}$ :  $(q_i, \tau_{o!}, \varphi, \lambda \cup \{y\}, q_{i_e}^{\alpha})$ ,  $(q_i, \tau_{o!}, \{y = \Delta\}, \{y\}, q_{i_e}^{\beta})$ ,  $(q_{i_e}^{\alpha}, o!, \{y \leq \Delta\}, \{y\}, q_{i+1})$  and  $(q_{i_e}^{\beta}, o!, \{y \leq \Delta\}, \{y\}, q_{i+1})$ . Since  $o!$  is firable in  $((q_i, r'_i), \hat{u}_i + a)$ , this means that  $F_{\text{rob}}((q_i, r'_i)) = o!$  and  $u_i + a \models [r'_i]_{\Delta}$ . Then by construction of  $F_{\text{rob}}$ ,  $F(q_{i_e}^{\alpha}, \tilde{r}'_i) = \tau_{o!}$ , and there exists  $b \in \mathbb{R}_{\geq 0}$  s.t.  $(q_i, \tilde{u}_i) \xrightarrow{b} (q_i, \tilde{u}_i + b)$  (concealing  $\tau_{o!}$  transitions), and  $\tilde{u}_i + b \in \tilde{r}'_i$ . Additionally,  $\forall \delta \in [-\Delta, \Delta]. (q_i, \tilde{u}_i) \xrightarrow{b+\delta} (q_i, \tilde{u}_i + b + \delta) \xrightarrow{o_1}$ . This is in particular the case for  $b + \delta = a$  since  $a \in [r'_i]_{\Delta}$ . Therefore  $(q_i, \tilde{u}_i) \xrightarrow{a} \xrightarrow{o_1} (q_{i+1}, \tilde{u}_{i+1})$ , and  $(q_{i+1}, \tilde{u}_{i+1})$  is winning since it is an outcome of  $F$ , and  $\tilde{u}_{i+1} = u_{i+1}$ , since the same clocks are reset. To finish the induction step, the same argument as in the first case is used to demonstrate that any input action firable from  $((q_i, r_i), u_i)$  after some delay  $d$  is also firable from  $(q_i, \tilde{u}_i)$ .  $\square$

This construction shall serve as a tool for deciding robust consistency, synthesizing a robust implementation, and other operations of the specification theory with robustness which are detailed in next section.

*Remark on completeness.* The robust game automaton is not a complete technique to solve robust timed games. Indeed, our notion of robustness introduces perturbations on the syntax of the automaton, whereas the robust game automaton modifies the semantics of the game. Therefore there exists specifications

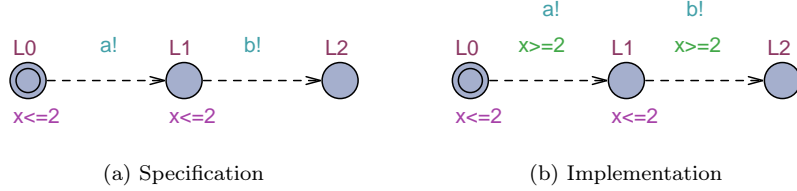


Figure 6: Specification with 1-robust implementation, but with no robust strategy in the robust game automaton.

that can be robustly implemented, although they will be judged as non robust using the robust game automaton construction. For instance, the specification in Fig. 6a can be robustly implemented with the implementation in Fig. 6b. But for  $\Delta = 1$  no robust strategy exists in the robust game automaton since clock  $x$  is not reset.

## 5. Robust Consistency and Robust Compatibility

### 5.1. Robust Consistency

We now provide a method to decide the  $\Delta$ -robust consistency of a specification and synthesize robust implementations by solving a robust timed safety game, in which the output player must avoid a set of immediate error states. From there, the computation of a robust strategy described in previous section provides a method to synthesize an implementation of the specification that is robust with respect to outputs enlargement.

To account for input restrictions, we increase the set of error states  $\text{err}^S$ . Intuitively a specification is  $\Delta$ -robust with respect to input  $i?$ , if between enabling of any two  $i?$  edges at least  $2\Delta$  time passes, during which the reaction to  $i?$  is unspecified. So, if the two transitions triggers  $\Delta$ -too-late and  $\Delta$ -too-early (respectively), there is no risk that the reaction is resolved non-deterministically in the specification.

In our input-enabled setup, lack of reaction is modeled using transitions to the universal (unpredictable) state. Formally, we say that  $\Delta$ -robust specifications should admit  $\Delta$ -latency of inputs. A state  $(q, u)$  verifies the  $\Delta$ -latency condition for inputs, iff for each edge  $e = (q, i?, \varphi, c, q')$ , where  $q' \neq l_u$  and  $e$  is enabled in  $(q, u)$  we have:

$$\forall d \in [0, 2\Delta]. \forall e' = (q, i?, \varphi, c, q'') \\ \text{if } e' \neq e \text{ and } (q, u) \xrightarrow{d} (q, u + d) \text{ and } e' \text{ is enabled in } (q, u + d) \text{ then } q'' = l_u$$

**Definition 15** For a specification  $S$  and  $\Delta \in \mathbb{R}_{>0}$ , the set  $\text{err}_\Delta^S$  of error states for  $\Delta$ -robust consistency is such that  $(q, u) \in \text{err}_\Delta^S$  if one the following conditions is verified:

- *Violates independent progress:*

$$(\exists d \in \mathbb{R}_{\geq 0}. (q, u) \xrightarrow{d}) \text{ and } (\forall d. \forall o!. (q, u) \xrightarrow{d} (q, u + d) \Rightarrow (q, u + d) \xrightarrow{o!})$$

- *Violates  $\Delta$ -latency of inputs:*  $\exists e = (q, i?, \varphi, c, q')$ , with  $q' \neq l_u$ , enabled in  $(q, u)$ , such that  $\exists d \in [0, 2\Delta]. (q, u) \xrightarrow{d} (q, u + d)$  and  $\exists e' = (q, i?, \varphi, c, q'')$  enabled in  $(q, u + d)$ , with  $e' \neq e$  and  $q'' \neq l_u$ .

Observe that  $\text{err}^S \subseteq \text{err}_{\Delta}^S$ , because the error condition with robustness is weaker than in the classical case (cf. page 12).

The  $\Delta$ -robust consistency game  $(\mathcal{S}, W^o(\text{err}_{\Delta}^S))$  can be solved using the construction of Definition 14. This synthesizes a robust strategy  $F$  and its syntactic outcome  $\mathcal{S}_F$ . Then we build the robust implementation  $\mathcal{I}_F$  by applying the following transformation to  $\mathcal{S}_F$ :

- When we apply a  $\Delta$ -perturbation on  $\mathcal{I}_F$ , a state  $((q, r), u)$  can be reached even if  $u \notin r \vee r \nearrow$ . However due to the region partitioning, the inputs edges available in  $\mathcal{I}_F$  might not be firable from  $r \vee r \nearrow$ . Then, in order to check the robust satisfaction relation between  $(\mathcal{I}_F)_{\Delta}$  and  $\mathcal{S}$ , we add additional input edges to  $\mathcal{I}_F$ : for each location  $(q, r)$  in  $\mathcal{I}_F$ , for each edge  $e = ((q, r), i?, \varphi, \lambda, (q^*, r^*))$  (with  $q^* \neq l_u$ ), and for each location  $(q', r')$  linked to  $(q, r)$  by a sequence of  $\tau$  transitions, we add an edge  $e' = ((q', r'), i?, \varphi, \lambda, (q^*, r^*))$ .
- To support restriction of input guards in  $(\mathcal{I}_F)_{\Delta}$ , we replaced in  $\mathcal{I}_F$  all guards  $\varphi$  of edges  $e = ((q, r), i?, \varphi, \lambda, (q', r'))$  with  $q' \neq l_u$  by their enlargement  $[\varphi]_{\Delta}$ . Guards on edges to the  $l_u$  location are adjusted in order to maintain action determinism and input-enableness.

Note that this construction adds many input edges to the implementation, out of which many are never enabled. This simplifies the construction and the proof of correctness. In practice, to efficiently synthesize implementations, coarser abstractions like zones should be used that do not include  $\tau$  transitions, thus avoiding the multiplication of input edges.

**Theorem 2** *Let  $\mathcal{S}$  be a specification. If  $F$  is a robust winning strategy in the  $\Delta$ -robust consistency game, then  $\mathcal{I}_F \text{ sat}_{\Delta} \mathcal{S}$  and  $\mathcal{S}$  is  $\Delta$ -robust consistent.*

**Proof 3 (Theorem 2)**  *$\mathcal{S}$  is  $\Delta$ -robust consistent if it admits a  $\Delta$ -robust implementation.  $\mathcal{I}_F$  verifies the independent progress condition since it corresponds to the outcome of the strategy  $F$  that avoids the inconsistent states in  $\mathcal{S}$ . For the same reason it also verifies the  $\Delta$ -latency condition, which permits to increase guards on input edges without adding non-determinism. Since  $F$  is a symbolic strategy it may authorize small delays in the regions where an output action must be fired, and therefore  $\mathcal{I}_F$  may not be output urgent. However, any point in these regions can be freely chosen to concretely implement  $\mathcal{I}_F$*

We check now that  $\mathcal{I}_F \text{ sat}_\Delta \mathcal{S}$  with the following relation

$$R = \{((q, r), u), (q, u) \in \llbracket (\mathcal{I}_F)_\Delta \rrbracket_{sem} \times \llbracket \mathcal{S} \rrbracket_{sem}\}$$

Note that since  $F$  is a robust winning strategy, the runs of  $[\mathcal{S}_F]_\Delta^o$  (and by construction the ones of  $(\mathcal{I}_F)_\Delta$ ) also belongs to  $\mathcal{S}$ . Finally we assume that  $\mathcal{S}$  can accept  $\tau$  transitions in any states as output transitions. Let consider  $((q, r), u), (q, u) \in R$ :

1. If  $((q, r), u) \xrightarrow{d}_{(\mathcal{I}_F)_\Delta} ((q, r), u + d)$  for some  $d \in \mathbb{R}_{\geq 0}$ , then since the runs of  $(\mathcal{I}_F)_\Delta$  are included into the ones of  $\mathcal{S}$ , it is also the case that  $(q, u) \xrightarrow{d}^S (q, u + d)$ .
2. If  $((q, r), u) \xrightarrow{o!}_{(\mathcal{I}_F)_\Delta} ((q', r'), u')$  for some  $o! \in Act_o$ , then there exists and edge  $((q, r), o!, \varphi, \lambda, (q', r'))$  in  $\mathcal{I}_F$  and also in  $\mathcal{S}_F$ . It also means that there exists a similar edge  $(q, o!, \varphi, \lambda, q')$  in  $\mathcal{S}$ . And since the runs of  $(\mathcal{I}_F)_\Delta$  are included into the ones of  $\mathcal{S}$ , it implies that  $(q, u) \xrightarrow{o!}^S (q', u')$ .
3. If  $((q, r), u) \xrightarrow{\tau}_{(\mathcal{I}_F)_\Delta} ((q, r'), u)$  then by assumption  $(q, u) \xrightarrow{\tau}^S (q, u)$ .
4. If  $(q, u) \xrightarrow{i?}^S (q', u')$  for some  $i? \in Act_i$ , then there exists and edge  $e = (q, i?, \varphi, \lambda, q')$  such that  $u \models \varphi$ . There also exists an edge  $(q, [u]) \xrightarrow{i?}^G (q', [u'])$  in the region graph of  $\mathcal{S}$ . If  $u \in r$  then this edge also exists in  $\mathcal{I}_F$ . Otherwise  $u \in [r \vee r \nearrow]_\Delta$ . This means that there exists a sequence of  $\tau$  transitions in  $\mathcal{I}_F$  between  $r$  and  $[u]$ , and by construction the input edge is copied in each location along this sequence, including  $(q, r)$ .  $u \models \varphi$  implies that  $u \models \llbracket [\varphi]_\Delta \rrbracket_\Delta$ , which proves that it is firable. Therefore  $((q, r), u) \xrightarrow{i?}^{I_\Delta} ((q', [u']), u')$ .  $\square$

## 5.2. Conjunction

A conjunction of two specifications captures the intersection of their implementation sets. The following conjunction operator has been proposed in [8]:

**Definition 16** Let  $\mathcal{S} = (Loc^S, q_0^S, Clk^S, E^S, Act, Inv^S)$  and  $\mathcal{T} = (Loc^T, q_0^T, Clk^T, E^T, Act, Inv^T)$  be specifications that share the same alphabet of actions  $Act$ . We define their conjunction, denoted  $\mathcal{S} \wedge \mathcal{T}$ , as the TIOA  $(Loc, q_0, Clk, E, Act, Inv)$  where  $Loc = Loc^S \times Loc^T$ ,  $q_0 = (q_0^S, q_0^T)$ ,  $Clk = Clk^S \uplus Clk^T$ ,  $Inv((q_s, q_t)) = Inv(q_s) \wedge Inv(q_t)$ , and the set of edges is defined by the following rule:

$$\begin{aligned} \text{if } (q_s, a, \varphi_s, \lambda_s, q'_s) \in E^S \text{ and } (q_t, a, \varphi_t, \lambda_t, q'_t) \in E^T \\ \text{then } ((q_s, q_t), a, \varphi_s \wedge \varphi_t, \lambda_s \cup \lambda_t, (q'_s, q'_t)) \in E \end{aligned}$$

It turns out that this operator is robust, in the sense of precisely characterizing also the intersection of the sets of *robust* implementations. So not only conjunction is the greatest lower bound with respect to implementation semantics, but also with respect to the robust implementation semantics. More precisely:

**Theorem 3** For specifications  $\mathcal{S}, \mathcal{T}$  and  $\Delta \in \mathbb{Q}_{>0}$ :

$$\llbracket \mathcal{S} \wedge \mathcal{T} \rrbracket_{mod}^\Delta = \llbracket \mathcal{S} \rrbracket_{mod}^\Delta \cap \llbracket \mathcal{T} \rrbracket_{mod}^\Delta$$

The theorem is a direct extension for robust implementations of Theorem 6 in [8]. We remark that due to the monotonicity of the refinement (Property 1), we can use two different delays  $\Delta_1$  and  $\Delta_2$ , such that:

$$\llbracket \mathcal{S} \rrbracket_{\text{mod}}^{\Delta_1} \cap \llbracket \mathcal{T} \rrbracket_{\text{mod}}^{\Delta_2} \supseteq \llbracket \mathcal{S} \wedge \mathcal{T} \rrbracket_{\text{mod}}^{\max(\Delta_1, \Delta_2)}$$

So requirements with different precision can be conjoined, by considering the smaller jitter. Robustness of the operator in Def. 16 is very fortunate. Thanks to this, large parts of implementation of theory of [8] can be reused.

### 5.3. Parallel Composition and Robust Compatibility

Composition is used to build systems from smaller units. Two specifications  $\mathcal{S}$ ,  $\mathcal{T}$  can be composed only iff  $\text{Act}_o^{\mathcal{S}} \cap \text{Act}_o^{\mathcal{T}} = \emptyset$ . Parallel composition is obtained in [8] by a product, where the inputs of one specification synchronize with the outputs of the other:

**Definition 17** Let  $\mathcal{S} = (\text{Loc}^{\mathcal{S}}, q_0^{\mathcal{S}}, \text{Clk}^{\mathcal{S}}, E^{\mathcal{S}}, \text{Act}^{\mathcal{S}}, \text{Inv}^{\mathcal{S}})$  and  $\mathcal{T} = (\text{Loc}^{\mathcal{T}}, q_0^{\mathcal{T}}, \text{Clk}^{\mathcal{T}}, E^{\mathcal{T}}, \text{Act}^{\mathcal{T}}, \text{Inv}^{\mathcal{T}})$  be two composable specifications.

We define their parallel product, denoted  $\mathcal{S} \parallel \mathcal{T}$ , as the TIOA  $(\text{Loc}, q_0, \text{Clk}, E, \text{Act}, \text{Inv})$ , where  $\text{Loc} = \text{Loc}^{\mathcal{S}} \times \text{Loc}^{\mathcal{T}}$ ,  $q_0 = (q_0^{\mathcal{S}}, q_0^{\mathcal{T}})$ ,  $\text{Clk} = \text{Clk}^{\mathcal{S}} \uplus \text{Clk}^{\mathcal{T}}$ ,  $\text{Act} = \text{Act}_o \cup \text{Act}_i$  with  $\text{Act}_o = \text{Act}_o^{\mathcal{S}} \uplus \text{Act}_o^{\mathcal{T}}$  and  $\text{Act}_i = (\text{Act}_i^{\mathcal{S}} \setminus \text{Act}_o^{\mathcal{T}}) \cup (\text{Act}_i^{\mathcal{T}} \setminus \text{Act}_o^{\mathcal{S}})$ ,  $\text{Inv}(q_s, q_t) = \text{Inv}(q_s) \wedge \text{Inv}(q_t)$ , and the set of edges is defined by the three following rules:

- If  $(q_s, a, \varphi_s, \lambda_s, q'_s) \in E^{\mathcal{S}}$  with  $a \in \text{Act}^{\mathcal{S}} \setminus \text{Act}^{\mathcal{T}}$  then each  $q_t \in \text{Loc}^{\mathcal{T}}$  gives rise to an edge  $((q_s, q_t), a, \varphi_s, \lambda_s, (q'_s, q_t)) \in E$ ;
- If  $(q_t, a, \varphi_t, \lambda_t, q'_t) \in E^{\mathcal{T}}$  with  $a \in \text{Act}^{\mathcal{T}} \setminus \text{Act}^{\mathcal{S}}$  then each  $q_s \in \text{Loc}^{\mathcal{S}}$  gives rise to an edge  $((q_s, q_t), a, \varphi_t, \lambda_t, (q_s, q'_t)) \in E$ ;
- If  $(q_s, a, \varphi_s, \lambda_s, q'_s) \in E^{\mathcal{S}}$  and  $(q_t, a, \varphi_t, \lambda_t, q'_t) \in E^{\mathcal{T}}$  with  $a \in \text{Act}^{\mathcal{S}} \cap \text{Act}^{\mathcal{T}}$  then this gives rise to an edge  $((q_s, q_t), a, \varphi_s \wedge \varphi_t, \lambda_s \cup \lambda_t, (q'_s, q'_t)) \in E$ .

**Example 5** The two timed specifications in Fig. 1a and 1b can be composed together by synchronizing the outputs `cof` and `tea` of the Machine with the inputs of the Researcher. The resulting TIOA is a timed specification whose input is coin and outputs are `pub`, `cof` and `tea`. This composition scheme is illustrated in the diagram of Fig. 7.

In the input-enableness setting we model incompatibility by introducing a predicate describing undesirable states denoted by the set `und`. For example, a communication failure can be modeled by redirecting an input edge to an undesirable location. In general any reachability objective, for example given by a temporal logic property, can serve as the set of undesirable behaviors `und`. It is important that such behaviors are avoided during the composition. For doing so, we propose to follow the optimistic approach to composition introduced in [9] that is *two specifications can be composed if there exists at least one environment*



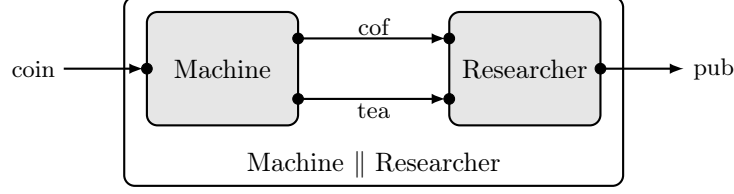


Figure 7: Diagram for the parallel composition of a coffee machine and a researcher

in which they can work together. In the robustness setting we consider imprecise environments by applying a  $\Delta$ -perturbation to their outputs. Then, in what follows, we say that a specification is  $\Delta$ -robust useful if there exists an imprecise environment  $\mathcal{E}$  that avoids the undesirable states, whatever the specification does.

**Definition 18** A specification  $\mathcal{S}$  is  $\Delta$ -robust useful if there exists an environment  $\mathcal{E}$  such that no undesirable states are reached in  $\llbracket [\mathcal{E}]_{\Delta}^{\circ} \parallel \mathcal{S} \rrbracket_{\text{sem}}$ .

**Property 4 (Monotonicity)** Given  $\Delta_1 \leq \Delta_2$ , if  $\mathcal{S}$  be a  $\Delta_2$ -robust useful specification, then  $\mathcal{S}$  is  $\Delta_1$ -robust useful.

**Proof 4 (Property 4)** If  $\mathcal{S}$  be a  $\Delta_2$ -robust useful this means that there exists an environment  $\mathcal{E}$  such that no undesirable states is reached in  $\llbracket [\mathcal{E}]_{\Delta_2}^{\circ} \parallel \mathcal{S} \rrbracket_{\text{sem}}$ . Then for  $\Delta_1 \leq \Delta_2$ ,  $\llbracket [\mathcal{E}]_{\Delta_1}^{\circ} \rrbracket_{\text{sem}} \subseteq \llbracket [\mathcal{E}]_{\Delta_2}^{\circ} \rrbracket_{\text{sem}}$ . This implies that  $\llbracket [\mathcal{E}]_{\Delta_1}^{\circ} \parallel \mathcal{S} \rrbracket_{\text{sem}} \subseteq \llbracket [\mathcal{E}]_{\Delta_2}^{\circ} \parallel \mathcal{S} \rrbracket_{\text{sem}}$ , which proves that  $\mathcal{S}$  is also  $\Delta_1$ -robust useful.  $\square$

To check robust usefulness we solve the robust game  $(\mathcal{S}, W^i(\text{und}))$ , and determine if the input player has a robust strategy  $F$  that avoids the undesirable states. Let  $\mathcal{S}_F$  be the syntactic outcome of  $F$  in  $\mathcal{S}$ . We build from  $\mathcal{S}_F$  a robust environment  $\mathcal{E}_F$  by permuting the input and output players, such that each input in  $\mathcal{S}_F$  becomes an output, and conversely.

**Theorem 4** Let  $\mathcal{S}$  be a specification. If  $F$  is a robust winning strategy in the  $\Delta$ -robust usefulness game, then  $\mathcal{S}$  is  $\Delta$ -robust useful in the environment  $\mathcal{E}_F$ .

**Proof 5 (Theorem 4)** The theorem directly follows from the definition of the robust strategy.  $\llbracket [\mathcal{S}_F]_{\Delta}^i \rrbracket_{\text{sem}} \subseteq \llbracket \mathcal{S} \rrbracket_{\text{sem}}$  which implies that  $[\mathcal{E}_F]_{\Delta}^{\circ}$  synchronizes with every action of  $\mathcal{S}$  in their parallel product. Therefore, only the states belonging to  $\llbracket [\mathcal{S}_F]_{\Delta}^i \rrbracket_{\text{sem}}$  can be reached in the composition, and by definition they are not undesirable.  $\square$

Finally, two specifications are compatible if their composition is useful.

**Definition 19** Two composable specifications  $\mathcal{S}$  and  $\mathcal{T}$  are  $\Delta$ -robust compatible if and only if  $\mathcal{S} \parallel \mathcal{T}$  is  $\Delta$ -robust useful.

We now study the impact of adding  $\Delta$ -perturbations introduced in previous section over parallel composition. It is important that the robust theory does not modify the definition of the operations themselves. This means that all the important properties of composition introduced in [8] remain valid. Moreover, robustness distributes over parallel composition in the following fashion:

**Lemma 1** *For any implementations  $\mathcal{I}$ ,  $\mathcal{J}$  and a delay  $\Delta \in \mathbb{Q}_{>0}$ :*

$$(\mathcal{I} \parallel \mathcal{J})_{\Delta} \leq \mathcal{I}_{\Delta} \parallel \mathcal{J}_{\Delta}$$

**Proof 6 (Lemma 1)** *In the following, we denote  $(Loc^k, q_0^k, Clk^k, E^k, Act^k, Inv^k)$ , with  $k \in \{I, J, I \parallel J\}$ , the TIOAs corresponding to  $\mathcal{I}, \mathcal{J}$ , or  $\mathcal{I} \parallel \mathcal{J}$ , respectively, and  $(St^k, (q_0^k, 0), Act^k, \rightarrow^k)$ , with  $k \in \{I, J, I \parallel J\}$ , their semantics, and with  $k \in \{I_{\Delta}, J_{\Delta}, [I \parallel J]_{\Delta}\}$ , their perturbed semantics.*

*First let recall Theorem 11 from [8] that states that  $\llbracket \mathcal{I}_{\Delta} \parallel \mathcal{J}_{\Delta} \rrbracket_{\text{sem}} = \llbracket \mathcal{I}_{\Delta} \rrbracket_{\text{sem}} \mid \llbracket \mathcal{J}_{\Delta} \rrbracket_{\text{sem}}$ , where  $\mid$  is the parallel composition between TIOAs. Then we need to prove the refinement  $\llbracket (\mathcal{I} \parallel \mathcal{J})_{\Delta} \rrbracket_{\text{sem}} \leq \llbracket \mathcal{I}_{\Delta} \rrbracket_{\text{sem}} \mid \llbracket \mathcal{J}_{\Delta} \rrbracket_{\text{sem}}$  by witnessing the following relation:*

$$R = \{(((q_i, q_j), u_{ij}), ((\hat{q}_i, u_i), (\hat{q}_j, u_j)) \in St^{[I \parallel J]_{\Delta}} \times (St^{I_{\Delta}} \times St^{J_{\Delta}}) \mid \\ ((q_i = \hat{q}_i \wedge u_i = u_{ij}|_{Clk^I}) \vee \hat{q}_i = l_u) \wedge ((q_j = \hat{q}_j \wedge u_j = u_{ij}|_{Clk^J}) \vee \hat{q}_j = l_u)\}$$

*We prove by coinduction that  $R$  is a timed alternating relation. Let  $((q_i, q_j), u_{ij}), ((\hat{q}_i, u_i), (\hat{q}_j, u_j)) \in R$ .*

1. *If  $((q_i, q_j), u_{ij}) \xrightarrow{d}_{[I \parallel J]_{\Delta}} ((q_i, q_j), u_{ij} + d)$  for some  $d \in \mathbb{R}_{\geq 0}$ , then by definition  $u_{ij} \models [Inv(q_i, q_j)]_{\Delta}$ . By construction of the parallel product  $[Inv(q_i, q_j)]_{\Delta} = [Inv(q_i) \wedge Inv(q_j)]_{\Delta} = [Inv(q_i)]_{\Delta} \wedge [Inv(q_j)]_{\Delta}$ . Then, we can deduce that  $u_i + d \models [Inv(q_i)]_{\Delta}$  and  $u_j + d \models [Inv(q_j)]_{\Delta}$ . This implies that  $(q_i, u_i) \xrightarrow{d}_{I_{\Delta}} (q_i, u_i + d)$  and  $(q_j, u_j) \xrightarrow{d}_{J_{\Delta}} (q_j, u_j + d)$ . Besides, by definition of the universal state, for any valuation  $u$  of a TIOA is always true that  $(l_u, u) \xrightarrow{d}(l_u, u + d)$ .*

*By definition of  $\llbracket \mathcal{I}_{\Delta} \rrbracket_{\text{sem}} \mid \llbracket \mathcal{J}_{\Delta} \rrbracket_{\text{sem}}$ ,  $((\hat{q}_i, u_i), (\hat{q}_j, u_j)) \xrightarrow{d}_{I_{\Delta} \parallel J_{\Delta}} ((\hat{q}_i, u_i + d), (\hat{q}_j, u_j + d))$ , and the relation  $R$  is trivially preserved in the next states.*

2. *If  $((q_i, q_j), u_{ij}) \xrightarrow{o!}_{[I \parallel J]_{\Delta}} ((q'_i, q'_j), u'_{ij})$ , then  $\exists e \in E^{I \parallel J}. e = ((q_i, q_j), o!, \varphi, c, (q'_i, q'_j))$  such that  $u_{ij} \models [\varphi]_{\Delta}$  and  $u'_{ij} \models [Inv(q'_i, q'_j)]_{\Delta}$ ,*

- (a) *And if  $o \in Act_o^I \setminus Act_o^J$  (or conversely, if  $o \in Act_o^J \setminus Act_o^I$ ; this case is similar, so we will not consider it), then  $\exists e_i \in E^I. e_i = (q_i, o!, \varphi_i, c_i, q'_i)$ , and  $q'_j = q_j$ ,  $\varphi = \varphi_i$ ,  $c = c_i$ , and  $u'_{ij}|_{Clk^J} = u_j$ . Consequently,  $u_i \models [\varphi_i]_{\Delta}$  and  $u'_i = u'_{ij}|_{Clk^I} \models [Inv(q'_i)]_{\Delta}$ , which proves that  $(q_i, u_i) \xrightarrow{o!}_{I_{\Delta}} (q'_i, u'_i)$ . Besides, by definition of the universal state,  $(l_u, u_i) \xrightarrow{o!}_{I_{\Delta}} (l_u, u_i)$ .*

*Then  $((\hat{q}_i, u_i), (\hat{q}_j, u_j)) \xrightarrow{o!}_{I_{\Delta} \parallel J_{\Delta}} ((\hat{q}'_i, u'_i), (\hat{q}_j, u_j))$ . Moreover, either  $\hat{q}_i = q_i$  and then  $\hat{q}'_i = q'_i$ , or  $\hat{q}_i = l_u = \hat{q}'_i$ , which proves that the relation  $R$  is preserved.*

(b) If  $o \in \text{Act}_o^I \cap \text{Act}_i^J$  (the reverse case  $o \in \text{Act}_o^J \cap \text{Act}_i^I$  is similar), then  $\exists e_i \in E^I. e_i = (q_i, o?, \varphi_i, c_i, q'_i)$  and  $\exists e_j \in E^J. e_j = (q_j, o!, \varphi_j, c_j, q'_j)$  and  $\varphi = \varphi_i \wedge \varphi_j$ ,  $c = c_i \cup c_j$ . On the side of  $\mathcal{I}$ , since  $u_{ij} \models [\varphi]_\Delta$  and  $u'_{ij} \models [\text{Inv}(q'_i, q'_j)]_\Delta$ , we get that  $u_i \models [\varphi_i]_\Delta$  and  $u'_i = u'_{ij}|_{\text{Cuk}^I} \models [\text{Inv}(q'_i)]_\Delta$ , which implies that  $(q_i, u_i) \xrightarrow{o!} I^\Delta(q'_i, u'_i)$ . On the side of  $\mathcal{J}$ , we also get that  $u_j \models [\varphi_j]_\Delta$  and  $u'_j \models [\text{Inv}(q'_j)]_\Delta$ . If moreover  $u_j \models [\varphi_j]_\Delta$ , then as previously it implies that  $(q_j, u_j) \xrightarrow{o!} J^\Delta(q'_j, u'_j)$ . Otherwise, a *reductio ab absurdum* argument allows us to prove that there exists no other edges  $e'_j = (q_j, o?, \varphi'_j, c'_j, q''_j) \in E^J$  such that  $u_j \models [\varphi'_j]_\Delta$  (since  $u_j \models [\varphi_j]_\Delta$  it implies that  $\exists \varepsilon \in [-\Delta, \Delta]. u_j + \varepsilon \models \varphi_j$ , but in the same time it would be the case that  $u_j + \varepsilon \models \varphi'_j$ . This is absurd since  $J$ , as an implementation is supposed to be deterministic). However, by construction of  $\llbracket J_\Delta \rrbracket_{\text{sem}}$ , input-enableness is preserved by linking the unexpected input to a universal location  $l_u$ . So  $(q_j, u_j) \xrightarrow{o!} J^\Delta(\text{univ}, u_j)$ . Then  $((\hat{q}_i, u_i), (\hat{q}_j, u_j)) \xrightarrow{o!} I^\Delta | J^\Delta((\hat{q}'_i, u'_i), (\hat{q}'_j, u'_j))$ , and as previously, by construction we check that the relation is preserved.

3. Finally, if  $((\hat{q}_i, \hat{u}_i), (\hat{q}_j, \hat{u}_j)) \xrightarrow{i?} I^\Delta | J^\Delta((\hat{q}'_i, u'_i), (\hat{q}'_j, u'_j))$ , for  $i \in \text{Act}_i^I \cap \text{Act}_i^J$  (the cases  $i \in \text{Act}_i^I \setminus \text{Act}_i^J$  or  $i \in \text{Act}_i^J \setminus \text{Act}_i^I$  can be proved similarly by considering that only one component reacts, while the other stay in the same state), then from the definition of the composition:

- $(\hat{q}_i, \hat{u}_i) \xrightarrow{i?} I(\hat{q}'_i, \hat{u}'_i)$ , and
- $(\hat{q}_j, \hat{u}_j) \xrightarrow{i?} J(\hat{q}'_j, \hat{u}'_j)$ .

Besides, due to input-enableness,  $((q_i, q_j), u_{ij}) \xrightarrow{i?} [I \parallel J]^\Delta((q_i, q_j), u_{ij} + d)$ , which implies that:

- $(q_i, u_i) \xrightarrow{i?} I(q'_i, u'_i)$ , with  $u'_i = u'_{ij}|_{\text{Cuk}^I}$ , and
- $(q_j, u_j) \xrightarrow{i?} J(q'_j, u'_j)$ , with  $u'_j = u'_{ij}|_{\text{Cuk}^J}$ .

If  $\hat{q}_i = l_u$  then  $\hat{q}'_i = l_u$ , and in this case the relation  $R$  is always preserved (and similarly for  $\hat{q}_j$ ). Otherwise  $\hat{q}_i = q_i$  and  $\hat{u}_i = u_i$  (and similarly for  $\hat{q}_j$ ). In this latter case, since  $\llbracket I_\Delta \rrbracket_{\text{sem}}$  is deterministic, it implies that  $\hat{q}'_i = q'_i$  and  $\hat{u}'_i = u'_i$ , which also proves the induction for relation  $R$ .  $\square$

Finally, we show in Theorem 5 that the independent implementability property of [8] can be extended to robust implementability, which follows from Lemma 1 and Theorem 10 in [8].

**Theorem 5** *Let  $\mathcal{S}$  and  $\mathcal{T}$  be composable specifications and let  $\mathcal{I}$  and  $\mathcal{J}$  be  $\Delta$ -robust implementations of  $\mathcal{S}$  and  $\mathcal{T}$  (resp.), i.e.  $\mathcal{I} \text{ sat}_\Delta \mathcal{S}$  and  $\mathcal{J} \text{ sat}_\Delta \mathcal{S}$ . Then  $\mathcal{I} \parallel \mathcal{J} \text{ sat}_\Delta \mathcal{S} \parallel \mathcal{T}$ . Moreover if  $\mathcal{S}$  and  $\mathcal{T}$  are  $\Delta$ -compatible then  $\mathcal{I}$  and  $\mathcal{J}$  are also  $\Delta$ -compatible.*

**Proof 7 (Theorem 5)** *The first part of the theorem is deduced from previous results:*

- Due to lemma 1,  $\llbracket (\mathcal{I} \parallel \mathcal{J})_\Delta \rrbracket_{\text{sem}} \leq \llbracket \mathcal{I}_\Delta \rrbracket_{\text{sem}} \mid \llbracket \mathcal{J}_\Delta \rrbracket_{\text{sem}}$ .
- Then due Theorem 10 in [8],  $\llbracket \mathcal{I}_\Delta \rrbracket_{\text{sem}} \mid \llbracket \mathcal{J}_\Delta \rrbracket_{\text{sem}} \leq \llbracket \mathcal{S} \rrbracket_{\text{sem}} \mid \llbracket \mathcal{T} \rrbracket_{\text{sem}}$ .
- Finally due Theorem 11 in [8],  $\llbracket \mathcal{S} \rrbracket_{\text{sem}} \mid \llbracket \mathcal{T} \rrbracket_{\text{sem}} = \llbracket \mathcal{S} \parallel \mathcal{T} \rrbracket_{\text{sem}}$ .

This proves  $\llbracket (\mathcal{I} \parallel \mathcal{J})_\Delta \rrbracket_{\text{sem}} \leq \llbracket \mathcal{S} \parallel \mathcal{T} \rrbracket_{\text{sem}}$ , and therefore  $\mathcal{I} \parallel \mathcal{J} \text{ sat}_\Delta \mathcal{S} \parallel \mathcal{T}$ .

We now prove the second part of the theorem. Since  $\mathcal{S}$  and  $\mathcal{T}$  are  $\Delta$ -compatible, there exists an environment  $\mathcal{E}$  such that  $\lceil \mathcal{E} \rceil_\Delta^\circ \parallel (\mathcal{S} \parallel \mathcal{T})$  avoids reaching any undesirable states. From Theorems 10 and 11 in [8], we get that  $\mathcal{I} \parallel \mathcal{J} \leq \mathcal{S} \parallel \mathcal{T}$ . Again with Theorem 10 we get that  $(\mathcal{I} \parallel \mathcal{J}) \parallel \lceil \mathcal{E} \rceil_\Delta^\circ \leq (\mathcal{S} \parallel \mathcal{T}) \parallel \lceil \mathcal{E} \rceil_\Delta^\circ$ . Consequently, since no undesirable states are reached in  $(\mathcal{S} \parallel \mathcal{T}) \parallel \lceil \mathcal{E} \rceil_\Delta^\circ$  this is also the case in  $(\mathcal{I} \parallel \mathcal{J}) \parallel \lceil \mathcal{E} \rceil_\Delta^\circ$ , which proves that  $\mathcal{I} \parallel \mathcal{J}$  is  $\Delta$ -useful and so  $\mathcal{I}$  and  $\mathcal{J}$  are  $\Delta$ -compatible.  $\square$

Additionally, due to the monotonicity of perturbations with respect to the refinement, two different delays can be used to implement specifications  $\mathcal{S}$  and  $\mathcal{T}$ . For two implementations  $\mathcal{I} \text{ sat}_{\Delta_1} \mathcal{S}$  and  $\mathcal{J} \text{ sat}_{\Delta_2} \mathcal{T}$  of the parallel components, their composition satisfies the composition of specifications with the smaller of the two precisions:

$$\mathcal{I} \parallel \mathcal{J} \text{ sat}_{\min(\Delta_1, \Delta_2)} \mathcal{S} \parallel \mathcal{T}$$

#### 5.4. Quotient

Quotient is a dual operator to composition, such that for a large specification  $\mathcal{T}$  and a small one  $\mathcal{S}$ ,  $\mathcal{T} \parallel \mathcal{S}$  is the specification of the components that composed with  $\mathcal{S}$  will refine  $\mathcal{T}$ . In other words,  $\mathcal{T} \parallel \mathcal{S}$  specifies the component that still needs to be implemented after having an implementation of  $\mathcal{S}$ , in order to build an implementation of  $\mathcal{T}$ . One possible application is when  $\mathcal{T}$  is a system specification, and  $\mathcal{S}$  is the plant, then a robust controller for a safety objective can be achieved by finding a  $\Delta$ -consistent implementation of the quotient  $\mathcal{T} \parallel \mathcal{S}$ .

To apply quotienting, we require that  $Act^S \subseteq Act^T$  and  $Act_o^S \subseteq Act_o^T$ . The construction of a quotient requires the use of a universal location  $l_u$ , as well as an inconsistent location  $l_\emptyset$  that forbids any outputs and forbids elapsing of time.

**Definition 20** Let  $\mathcal{S} = (Loc^S, q_0^S, Clk^S, E^S, Act^S, Inv^S)$  and  $\mathcal{T} = (Loc^T, q_0^T, Clk^T, E^T, Act^T, Inv^T)$  be two specifications, with  $Act^S \subseteq Act^T$  and  $Act_o^S \subseteq Act_o^T$ .

Their quotient, denoted  $\mathcal{T} \parallel \mathcal{S}$ , is the TIOA  $(Loc, q_0, Clk, E, Act, Inv)$  where  $Loc = Loc^T \times Loc^S \cup \{l_u, l_\emptyset\}$ ,  $q_0 = (q_0^T, q_0^S)$ ,  $Clk = Clk^T \uplus Clk^S \uplus \{x_{new}\}$ ,  $Act = Act_i \uplus Act_o$  with  $Act_i = Act_i^T \cup Act_o^S \cup \{i_{new}\}$  and  $Act_o^T \setminus Act_o^S$ ,  $Inv(q_t, q_s) = Inv(l_u) = \text{true}$  and  $Inv(l_\emptyset) = \{x_{new} \leq 0\}$ , and the set  $E$  of edges is defined by the following rules:

- $\forall q_t \in Loc^T. \forall q_s \in Loc^S. \forall a \in Act. \exists((q_t, q_s), a, \neg Inv^S(q_s), \{x_{new}\}, l_u) \in E$ ,
- $\forall q_t \in Loc^T. \forall q_s \in Loc^S. \exists((q_t, q_s), i_{new}, \neg Inv(q_t) \wedge Inv(q_s), \{x_{new}\}, l_\emptyset) \in E$ ,
- If  $(q_t, a, \varphi_t, \lambda_t, q'_t) \in E^T$  and  $(q_s, a, \varphi_s, \lambda_s, q'_s) \in E^S$ , then  $\exists((q_t, q_s), a, \varphi^T \wedge \varphi^S, \lambda_t \cup \lambda_s, (q'_t, q'_s)) \in E$ ,

- $\forall (q_s, a, \varphi_s, \lambda_s, q'_s) \in E^S$  with  $a \in Act_o^S$ ,  $\exists((q_t, q_s), a, \varphi^S \wedge \neg G^T, \{x_{new}\}, l_\emptyset) \in E$ , where  $G^T = \bigvee \{\varphi_t \mid (q_t, a, \varphi_t, \lambda_t, q'_t)\}$ ,
- $\forall (q_t, a, \varphi_t, \lambda_t, q'_t) \in E^T$  with  $a \notin Act^S$ ,  $\exists((q_t, q_s), a, \varphi^T, \lambda_t, (q'_t, q'_s)) \in E$ ,
- $\forall (q_t, a, \varphi_t, \lambda_t, q'_t) \in E^T$  with  $a \in Act_o^S$ ,  $\exists((q_t, q_s), a, \neg G^T, \{\}, l_u) \in E$ , where  $G^T = \bigvee \{\varphi_s \mid (q_s, a, \varphi_s, \lambda_s, q'_s)\}$ ,
- $\forall a \in Act_i. \exists(l_\emptyset, a, x_{new} = 0, \emptyset, l_\emptyset) \in E$ ,
- $\forall a \in Act. \exists(l_u, a, true, \emptyset, l_u) \in E$ .

As stated in Theorem 12 of [8], the quotient gives a maximal (the weakest) specification for a missing component. This theorem can be generalized to specifications that are locally consistent (see [8]), and used to argue for completeness of the quotient construction in the robust case. It turns out that this very operator is also maximal for the specification of a robust missing component, in the following sense:

**Theorem 6** *Let  $\mathcal{S}$  and  $\mathcal{T}$  be two specifications such that the quotient  $\mathcal{T} \parallel \mathcal{S}$  is defined and let  $\mathcal{J}$  be an implementation, then:*

$$\mathcal{S} \parallel \mathcal{J}_\Delta \leq \mathcal{T} \quad \text{iff} \quad \mathcal{J} \text{ sat}_\Delta \mathcal{T} \parallel \mathcal{S}$$

**Proof 8 (Theorem 6)** *First let remark that  $\mathcal{J}_\Delta$  is a locally consistent specification, as defined in [8]. Then, we can apply Theorem 12 of [8] to  $\mathcal{J}_\Delta$  which proves:*

$$\mathcal{S} \parallel \mathcal{J}_\Delta \leq \mathcal{T} \quad \text{iff} \quad \mathcal{J}_\Delta \leq \mathcal{T} \parallel \mathcal{S}$$

*According to the definition of  $\Delta$ -robust satisfaction this proves the theorem.*

## 6. Counter Strategy Refinement For Parametric Robustness

In the previous sections we define and solve robustness problems for a fixed delay, and we study the properties of these perturbations with respect to the different operators in the specification theory. To complete this work we propose a technique that evaluates the greatest possible value of the perturbation. We follow a counterexample refinement approach, a technique used for abstraction refinement and called CEGAR in [28]. In our setting counterexamples are spoiling strategies computed for a given value of the perturbation. We replay these strategies on a parametric model of the robust game in order to refine the value of the perturbation.

The robustness problems that we consider in this sections are the parametric extension of the previously defined problems:

*Robust Consistency.* Given a specification  $\mathcal{S}$ , determine the greatest value of  $\Delta$  such that  $\mathcal{S}$  is  $\Delta$ -robust consistent.

*Robust Usefulness.* Given a specification  $\mathcal{S}$ , determine the greatest value of  $\Delta$  such that  $\mathcal{S}$  is  $\Delta$ -robust useful.

### 6.1. Parametric Timed Games

When we consider  $\Delta$  as a free parameter, the robust game automaton construction of Section 4 defines a Parametric Timed I/O Automata, in a similar manner as Parametric Timed Automata are defined in [29, 30]. We denote by  $\Phi_\Delta(\text{Clk})$  the set of parametric guards with parameter  $\Delta$  over a set of clocks  $\text{Clk}$ . Parametric guards in  $\Phi_\Delta(\text{Clk})$  are generated by the following grammar  $\varphi ::= x \prec l \mid x - y \prec l \mid \varphi \wedge \varphi$ , where  $x, y \in \text{Clk}$ ,  $\prec \in \{<, \leq, >, \geq\}$  and  $l = a + b * \Delta$  is a linear expression such that  $a, b \in \mathbb{Q}$ .

**Definition 21** A Parametric TIOA with parameter  $\Delta$ , is a TIOA  $\mathcal{A}$  such that guards and invariants are replaced by parametric guards.

For a given value  $\delta \in \mathbb{Q}$  of the parameter, we define the non-parametric game  $\mathcal{A}_\delta$  obtained by replacing each occurrence of the parameter  $\Delta$  in the parametric guards of  $\mathcal{A}$  by the value  $\delta$ .

A parametric symbolic state  $X$  is a set of triple  $(q, u, \delta)$ , where  $\delta$  is a value of the parameter  $\Delta$  and  $(q, u)$  is a state in  $\llbracket \mathcal{A}_\delta \rrbracket_{\text{sem}}$ . Operations on symbolic states can be extended to parametric symbolic states, such that  $X \nearrow^P$ ,  $X \searrow^P$ ,  $\text{PPost}_a(X)$ ,  $\text{PPred}_a(X)$  and  $\text{PPred}_t(X, Y)$  stands for the extensions of previously defined non-parametric operations. Formally:

$$\begin{aligned} X \nearrow^P &= \{(q, u + d, \delta) \mid (q, u, \delta) \in X, d \in \mathbb{R}_{\geq 0}\} \\ X \searrow^P &= \{(q, u - d, \delta) \mid (q, u, \delta) \in X, d \in \mathbb{R}_{\geq 0}\} \\ \text{PPost}_a(X) &= \{(q', u', \delta) \mid \exists (q, u, \delta) \in X. (q, u) \xrightarrow{a, \mathcal{A}_\delta} (q', u')\} \\ \text{PPred}_a(X) &= \{(q, u, \delta) \mid \exists (q', u', \delta) \in X. (q, u) \xrightarrow{a, \mathcal{A}_\delta} (q', u')\} \\ \text{PPred}_t(X, Y) &= \{(q, u, \delta) \mid \exists d \in \mathbb{R}_{\geq 0}. (q, u) \xrightarrow{d, \mathcal{A}_\delta} (q, u + d) \text{ and } (q, u + d) \in X \\ &\quad \text{and } \forall d' \in [0, d]. (q, u + d', \delta) \notin Y\} \end{aligned}$$

### 6.2. Parametric Robustness Evaluation

Let  $\Delta_{max}$  be the theoretical maximum value for  $\Delta$  is the robust game (or its supremum). Solving the robustness problems for any value of  $\Delta$  would in general require to solve a parametric timed game. This problem is undecidable as it has been shown that parametric model-checking problem is undecidable [29]. In this paper we propose to compute an approximation of this maximum value. Due to the monotonicity of the robustness problems (Properties 1 and 4), we can apply an iterative evaluation procedure that searches for the maximum value until it belongs within a given precision interval. This basic procedure is describe in Algorithm 1 for the parametric game  $(\mathcal{A}_{\text{rob}}^\Delta, W^\circ(\text{Bad}))$  for output (again it applies symmetrically to input).

The algorithm assumes that the game  $(\mathcal{A}_{\text{rob}}^0, W^\circ(\text{Bad}))$  is won, and on contrary that  $(\mathcal{A}_{\text{rob}}^{\Delta_{init}}, W^\circ(\text{Bad}))$  is lost. At the heart of the algorithm the

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**Algorithm 1:** Evaluation of the maximum robustness

---

**Input:**  $(\mathcal{A}_{\text{rob}}^\Delta, W^o(\text{Bad}))$ : parametric robust timed game,  
 $\Delta_{\text{init}}$ : initial maximum value,  
 $\epsilon$ : precision  
**Output:**  $\Delta_{\text{good}}$  such that  $\Delta_{\text{max}} - \Delta_{\text{good}} \leq \epsilon$

```
1 begin
2    $\Delta_{\text{good}} \leftarrow 0$ 
3    $\Delta_{\text{bad}} \leftarrow \Delta_{\text{init}}$ 
4   while  $\Delta_{\text{bad}} - \Delta_{\text{good}} > \epsilon$  do
5      $(\Delta_{\text{good}}, \Delta_{\text{bad}}) \leftarrow \text{RefineValues}(\mathcal{A}_{\text{rob}}^\Delta, \Delta_{\text{good}}, \Delta_{\text{bad}})$ 
6   end
7   return  $\Delta_{\text{good}}$ 
8 end
```

---

procedure **RefineValues** plays the game for a chosen value, and update the variables  $\Delta_{\text{good}}$  and  $\Delta_{\text{bad}}$  according to the result.

Different algorithms can be used to implement **RefineValues**. A basic method is binary search. In that case **RefineValues** chooses the middle point  $\Delta_{\text{mid}}$  of the interval  $[\Delta_{\text{good}}, \Delta_{\text{bad}}]$  and plays the game  $(\mathcal{A}_{\text{rob}}^{\Delta_{\text{mid}}}, W^o(\text{Bad}))$ . According to the results it updates either  $\Delta_{\text{good}}$  or  $\Delta_{\text{bad}}$ . This algorithm has several drawbacks. First, the number of games it needs to solve heavily depends on the precision parameter. Second, depending on the initial maximum value a high proportion of the games played may be winning, which implies that they explore completely the state-graph of the model.

*Correctness and termination.* The algorithm is correct if two invariants are satisfied:  $\Delta_{\text{good}}$  is a lower bound for  $\Delta_{\text{max}}$ , and  $\Delta_{\text{bad}}$  is a greater bound. These invariants must be preserved by the implementation of **RefineValues**.

The algorithm terminates if at each iteration **RefineValues** reduces the length of the interval  $[\Delta_{\text{good}}, \Delta_{\text{bad}}]$  by some fixed minimum amount.

### 6.3. Counter Strategy Refinement

We propose an alternative method that analyzes the spoiling strategies computed when the game is lost and refine the value of the variable  $\Delta_{\text{bad}}$ . With this algorithm only the last game is winning. The different steps are the following.

1. Play the game  $(\mathcal{A}_{\text{rob}}^{\Delta_{\text{bad}}}, W^o(\text{Bad}))$ .
2. If the game is won, return the values  $(\Delta_{\text{bad}}, \Delta_{\text{bad}})$ .
3. Else extract a counter strategy  $F_i$  for the input player.
4. Replay  $F_i$  on the parametric game using Algorithm 2; it returns a value  $\Delta_{\text{min}}$ .
5. If  $\Delta_{\text{min}}$  is only an infimum and  $\Delta_{\text{bad}} - \Delta_{\text{min}} > \epsilon$ , return the values  $(\Delta_{\text{good}}, \Delta_{\text{min}})$ .
6. Else return the values  $(\Delta_{\text{good}}, \Delta_{\text{min}} - \epsilon)$ .

The goal of Algorithm 2 is to replay the spoiling strategy  $F_i$  on the parametric game and compute the maximum value of  $\Delta$  such that this strategy becomes unfeasible. It takes as inputs the parametric game automaton  $\mathcal{A}_{\text{rob}}^\Delta$ , the symbolic graph  $(\mathcal{Z}_A^{\Delta_{\text{bad}}}, X_0, \rightarrow)$  computed for the game  $(\mathcal{A}_{\text{rob}}^\Delta, W^\circ(\text{Bad}))$ , and the spoiling strategy  $F_i$ . It returns the infimum of the values  $\Delta_{\text{bad}}$  such that  $F_i$  is a spoiling strategy in the game  $(\mathcal{A}_{\text{rob}}^\Delta, W^\circ(\text{Bad}))$ .

The algorithm is similar to the timed game algorithm proposed in [23] and implemented in the tool TIGA [31]. However only the backward analysis is applied on parametric symbolic states, starting from the "bad" locations. Additionally the algorithm only explores the states that belongs to the outcome of  $F_i$ . Since  $F_i$  is a spoiling strategy in a safety game, its outcome contains a set of finite runs that eventually reach the "bad" locations. This ensures that a backward exploration restricted to this set of finite runs will terminate.

Formally, we define the outcome of symbolic spoiling strategy  $F_i$  for input. First, for a symbolic state  $X$  we define its timed successors restricted by the symbolic strategy  $F_i$  (defined on a set of zones  $\mathcal{Z}$ ) as:

$$\begin{aligned} X \nearrow^{F_i} = & \{(q, u + d) \mid (q, u) \in X, d \in \mathbb{R}_{\geq 0}, \forall d' \in [0, d]. \\ & \text{if } \exists Z \in \mathcal{Z}. \exists Z' \in \mathcal{Z}. u + d \in Z. u + d' \in Z' \\ & \text{then } F((q, Z)) = F((q, Z')) = \text{delay}\} \end{aligned}$$

$\text{Outcome}(F_i)$  is the subset of runs in the symbolic graph defined inductively by:

- $(q_0, S_0 \nearrow^{F_i}) \in \text{Outcome}(F_i)$ ,
- if  $\rho \in \text{Outcome}(F_i)$  and  $\text{last}(\rho) = (q, Z)$ , then  $\rho' = \rho \rightarrow (q', Z') \in \text{Outcome}(F_i)$  if  $\exists (q, a, \varphi, \lambda, q') \in E$  and one of the following condition holds:
  1. either  $a \in \text{Act}_i$  and  $\exists Z'' . F_i(Z'') = a$  and  $Z' = \text{Post}_a(Z \cap Z'') \nearrow^{F_i}$ ,
  2. or  $a \in \text{Act}_o$  and  $\exists Z'' . F_i(Z'') = \text{delay}$  and  $Z' = \text{Post}_a(Z \cap Z'') \nearrow^{F_i}$ ,

The backward exploration ends when the set of winning states  $PWin[X_0]$  contains the initial state. Then, the projection  $(PWin[X_0] \cap \mathbf{0})_{|\Delta}$  computes the set of all the valuations of  $\Delta$  such that the strategy  $F_i$  is winning. The algorithm returns the infimum of these valuations.

**Theorem 7 (Soundness)** *Algorithm 2 returns a set of values  $\Gamma \subseteq \mathbb{Q}$  such that  $\forall \delta \in \Gamma$  the game  $(\mathcal{A}_{\text{rob}}^\delta, W^\circ(\text{Bad}))$  is lost.*

**Proof 9 (Theorem 7)** *For a value  $\delta \in \mathbb{Q}$ , Algorithm 2 is similar to the timed games algorithm of TIGA described in [23], but with less exploration steps since only the outcome of the spoiling strategy are explored. Therefore for that value  $PWin[X_0] \subseteq Win[X_0]$ .*

*Ab absurdo, if  $\delta \in \Gamma$  is a good value, i.e.  $(\mathcal{A}_{\text{rob}}^\delta, W^\circ(\text{Bad}))$  is winning, then  $\mathbf{0} \notin Win[X_0]$ , and consequently  $\mathbf{0} \notin PWin[X_0]$ . This implies that  $\Gamma = \text{Minimize}((PWin[X_0] \cap \mathbf{0})_{|\Delta}) = \emptyset$ , which is a contradiction.*

Theorem 7 ensures that `RefineValues` preserves the invariants of Algorithm 1.



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**Algorithm 2:** Counter strategy refinement

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**Input:**  $(\mathcal{A}_{\text{rob}}^\Delta, W^\circ(\text{Bad}))$ : parametric robust timed game,  
 $(\mathcal{Z}_{\mathcal{A}}^{\Delta_{\text{bad}}}, X_0, \rightarrow)$ : symbolic graph computed for  $(\mathcal{A}_{\text{rob}}^{\Delta_{\text{bad}}}, W^\circ(\text{Bad}))$   
 $F_i$ : spoiling strategy for input in  $(\mathcal{A}_{\text{rob}}^{\Delta_{\text{bad}}}, W^\circ(\text{Bad}))$   
**Output:** Infimum of the values  $\Delta_{\text{bad}}$  such that  $F_i$  is a spoiling strategy in  $(\mathcal{A}_{\text{rob}}^{\Delta_{\text{bad}}}, W^\circ(\text{Bad}))$

```
1 begin
  /* Initialisation */
2   Waiting  $\leftarrow \emptyset$ 
3   for  $X = (q, Z) \in \mathcal{Z}_{\mathcal{A}}$  do
4     if  $q \in \text{Bad}$  then
5        $PWin[X] \leftarrow \llbracket Inv(q) \rrbracket$ 
6       Waiting  $\leftarrow \text{Waiting} \cup \{Y \mid \exists \rho. \rho \rightarrow Y \rightarrow X \in \text{Outcome}(F_i)\}$ 
7     else
8        $PWin[X] \leftarrow \emptyset$ 
9     end
10  end
  /* Backward exploration */
11  while (Waiting  $\neq \emptyset$ )  $\wedge \mathbf{0} \notin PWin[X_0]$  do
12     $X = (q, Z) \leftarrow \text{pop}(\text{Waiting})$ 
13     $PBad^* \leftarrow \neg \llbracket Inv(q) \rrbracket \cup (\bigcup_{X \xrightarrow{a \in Act_i} Y} \text{PPred}_a(Win[Y]))$ 
14     $PGood^* \leftarrow \bigcup_{X \xrightarrow{a \in Act_o} Y} \text{PPred}_a(\llbracket Inv(Y) \rrbracket \setminus PWin[Y])$ 
15     $PWin[X] \leftarrow \text{PPred}_t(PBad^*, PGood^* \setminus PBad^*)$ 
16    Waiting  $\leftarrow \text{Waiting} \cup \{Y \mid \exists \rho. \rho \rightarrow Y \rightarrow X \in \text{Outcome}(F_i)\}$ 
17  end
18  return  $\text{Minimize}((PWin[X_0] \cap \mathbf{0})_{|\Delta})$ 
19 end
```

---

## 7. Implementation and experiments

### 7.1. PyECDAR implementation

The specification theory described in [8] is implemented in the tool ECDAR [32]. In order to experiment the methods proposed in this paper we have built a prototype in Python that reimplements the main functionalities of ECDAR and support the analysis of the robustness of timed specifications [33]. Inside this tool, the theory presented in Sections 4 and 5 is implemented as a set of model transformations:

1. Computation of  $\mathcal{I}_\Delta$ , the  $\Delta$ -perturbation of an implementation  $\mathcal{I}$  for some  $\Delta \in \mathbb{Q}_{\geq 0}$ .
2. Computation of the robust game automaton  $\mathcal{A}_{\text{rob}}^\Delta$ .
3. In order to add rational perturbations on the models  $\mathcal{I}_\Delta$  and  $\mathcal{A}_{\text{rob}}^\Delta$  the tool scales all the constants in the TIOA.

4. Finally we transform the TIOA of a specification into a specific *consistency game automaton* (resp. *usefulness game automaton*), such that all non  $\Delta$ -robust consistent (resp. non  $\Delta$ -robust useful) states are observed by a single location.

By combining these transformations we can check in the tool the three problems:  $\Delta$ -robust satisfaction,  $\Delta$ -consistency and  $\Delta$ -usefulness. The algorithms used are respectively the alternating simulation algorithm presented in [23] and the on-the-fly timed game algorithm presented in [25].

To solve the parametric robustness problems we have implemented the heuristic presented in Section 6 that approximates the maximum solution through a counter strategy refinement, and we have implemented a binary search heuristic to compare the efficiency. In the algorithm 2 operations on parametric symbolic states are handled with the Parma Polyhedra Library [34]. We shall remark that using polyhedra increases the complexity of computations compared to Difference Bound Matrices (DBMs), but this is necessary due to the form of the parametric constraints that are beyond the scope of classical DBMs. This not so much a problem in our approach as parametric analysis is limited to spoiling strategies whose size is kept as small as possible. Nevertheless an interesting improvement can be to use Parametric DBMs as presented in [30].

## 7.2. Experiments

We evaluate the performances of the tool to solve the parametric robustness problems on two academic examples. We compare in these experiments the Counter strategy Refinement (CR) approach with the Binary Search (BS) method. We presents benchmarks results for different values of the initial parameters  $\Delta_{init}$  and  $\epsilon$ .

*Specification of a university:* . The toy examples featured in this paper are extracted from [8]. They are part of an overall specification of a university, composed by three specifications: the coffee machine (M) of Fig. 1a, the researcher (R) of Fig. 1b, and the Administration (A) (see [8]). We study the robust consistency and the robust compatibility of these specifications and their parallel composition. The results are presented in Tables 1 and 2. The column *game size* displays the size of the robust game automaton used in the analysis in terms of locations (*loc.*) and transitions (*trans.*). The next columns display the time spent to compute the maximum perturbation with different initial conditions. The analysis of these results first shows that the Counter strategy Refinement method is almost independent from the two initial parameters  $\Delta_{init}$  and  $\epsilon$ . This is not the case for Binary Search: the precision  $\epsilon$  influences the number of games that must be solved, and the choice of  $\Delta_{init}$  change the proportion of games that are winning. Comparing the results of the two methods shows that for most of the cases, especially the more complex one, the Counter strategy Refinement approach is more efficient.

Model	Game size loc. trans.		$\Delta_{init} = 8$		$\Delta_{init} = 6$		$\Delta_{init} = 8$		$\Delta_{init} = 6$	
			$\epsilon = 0.1$		$\epsilon = 0.1$		$\epsilon = 0.01$		$\epsilon = 0.01$	
			CR	BS	CR	BS	CR	BS	CR	BS
<b>M</b>	9	21	119ms	314ms	119ms	262ms	119ms	438ms	119ms	437ms
<b>R</b>	11	27	188ms	303ms	188ms	299ms	188ms	419ms	188ms	523ms
<b>A</b>	9	22	133ms	316ms	133ms	287ms	133ms	441ms	133ms	483ms
<b>M    A</b>	41	158	10.1s	10.1s	10.1s	9.6s	10.4s	17.5s	10.4s	17.6s
<b>R    A</b>	48	201	14.1s	12.1s	12.5s	11s	14.1s	19.6s	12.5s	19.4s
<b>M    R</b>	44	152	10s	15.5s	9.81s	15.8s	10.3s	22.9s	9.78s	29.2s
<b>M    R    A</b>	180	803	54.4s	56.3s	54.6s	112s	55s	58.8s	55.7s	216s

Table 1: Robust consistency of the university specifications

Model	Game size loc. trans.		$\Delta_{init} = 8$		$\Delta_{init} = 6$		$\Delta_{init} = 8$		$\Delta_{init} = 6$	
			$\epsilon = 0.1$		$\epsilon = 0.1$		$\epsilon = 0.01$		$\epsilon = 0.01$	
			CR	BS	CR	BS	CR	BS	CR	BS
<b>M    R</b>	21	90	2.64s	4.34s	1.72s	4.02s	2.64s	5.5s	1.72s	5.45s
<b>M    R    A</b>	75	399	48s	65s	42.7s	74.2s	48.2s	78.1s	42.9s	120s

Table 2: Robust compatibility between the university specifications

*Specification of a Milner Scheduler:* . The second experiment studies a real time version of Milner’s scheduler previously introduced in [32]. The model consists in a ring of  $N$  nodes. Each nodes receives a start signal from the previous node to perform some work and in the mean time forward the token to the next node within a given time interval. We check the robust consistency of this model for different values of  $N$  and different initial parameters. The results are displayed in Table 3. Like in previous experiment, the results show that the Counter strategy Refinement method is independent form the initial conditions and in general more efficient than Binary Search.

### 7.3. Interpretation

Previous results are sum up in Fig. 8 in order to compare the performances of the two methods on the most complex examples.

The performances of the Binary Search method depends on the number of games that are solved and on the outcome of these games. Games that are

Model	Game size loc. trans.		$\Delta_{init} = 30$		$\Delta_{init} = 31$		$\Delta_{init} = 30$		$\Delta_{init} = 31$	
			$\epsilon = 0.5$		$\epsilon = 0.5$		$\epsilon = 0.1$		$\epsilon = 0.1$	
			CR	BS	CR	BS	CR	BS	CR	BS
<b>1 Node</b>	13	35	0.97s	0.68s	1.09s	0.72s	0.97s	1.03s	1.09s	1.09s
<b>2 Nodes</b>	81	344	10.7s	10.3s	11.2s	12.6s	10.5s	15.8s	11.1s	19.4s
<b>3 Nodes</b>	449	2640	1m58	2m25	2m06	2m26	1m57	3m39	2m05	3m45
<b>4 Nodes</b>	2305	17152	17m38	24m12	17m38	27m46	17m41	37m57	17m37	41m50

Table 3: Robust consistency of Milner’s scheduler nodes

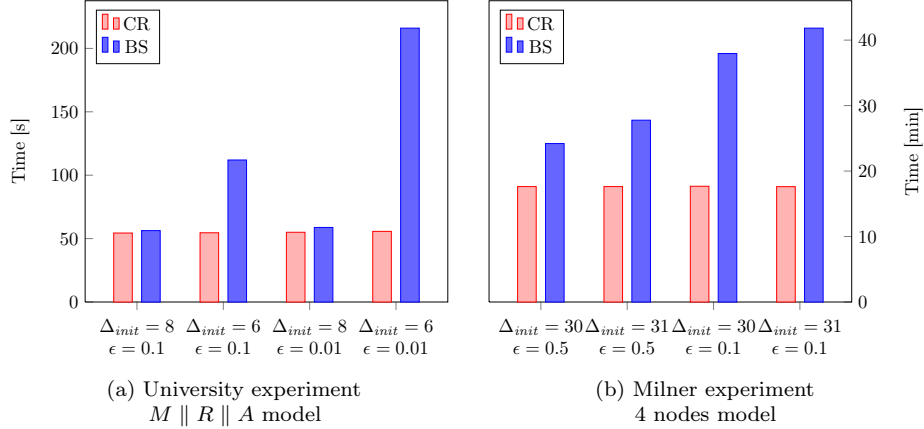


Figure 8: Comparisons of the performances between the two methods Counter strategy Refinement (CR) and Binary Search (BS)

winning (or games that are losing but with a value of  $\Delta$  close to the optimum value) are harder to solve, since in these cases the (almost) complete symbolic state space must be explored. Reducing the precision parameter  $\epsilon$  implies that more games must be solved close to the optimum value, and therefore it increases the time of analysis. Moreover, changing, even slightly, the initial maximum value  $\Delta_{init}$  may change the number of games, but most important the outcome of these games, and therefore the proportion of winning games. For instance in the last experiment, the expected result is 7.5. With an initial value of 30 the bisections performed by the Binary Search method arbitrarily imply that only 1 game is winning out of 9 (for  $\epsilon = 0.1$ ). With 31 this proportion is 6 out of 9, which increases the complexity of the analysis.

With the Counter strategy Refinement approach proposed in this paper only losing games are played until one is winning. The choice of  $\Delta_{init}$  modifies the number of games that are solved, but in general the first games for large values of  $\Delta$  are easily solved. Consequently, the choice of  $\Delta_{init}$  shows in the experiments almost no impact on the performances. With the parametric approach the parameter  $\epsilon$  is only used when the value  $\Delta_{min}$  computed by the refinement process is the minimum of the bad values. In that case the next iteration plays the game with the value  $\Delta_{min} - \epsilon$ . The experiments shows this has no impact on the performances.

## 8. Concluding Remarks

We have presented a compositional framework for reasoning about robustness of timed I/O specifications. Our theory builds on the results presented in [8] combined together with a new robust timed game for robust specification theories. It can be used to synthesize an implementation that is robust with respect to a

given specification, and to combine or compare specifications in a robust manner. In our approach, robustness is achieved through syntactic transformations, which allows reusing classical analysis technique and tools. In particular we extend the construction of [11] to the setting of specification theories, to solve robust games by reducing them to problems on classical timed games.

As a new contribution from [22], we also study the parametric robustness problems and evaluate the maximum imprecision allowed by specifications. To this end we propose a counter example refinement approach that analyzes spoiling strategies in timed games. These contributions have been implemented in a prototype tool that has been used to evaluate the performances of our counter strategy refinement approach.

We have focused in this paper on solving robust consistency and robust compatibility problems. This provides a constructive approach to synthesize robust implementations. In a future version of our tool we would like to apply the counter example refinement approach on the alternating simulation game, in order to solve the parametric satisfaction problem for an existing implementation.

In future works we also plan to extend our approach to different models, like timed automata with stochastic semantics [35]. In this context we could give a stochastic definition of robustness that would allow a more expressive quantitative analysis than the worst case scenario used in this paper.

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