	Monday 28th	Tuesday 29th	Wednesday 30th	Thursday 1st	Friday 2nd
7h30-8h30	Breakfast (if arrived on Sunday)	Breakfast	Breakfast	Breakfast	Breakfast
9h-9h45	Registration, Welcome & Coffee	Boris Düdder, DIKU Denmark	Guillaume Baudart, INRIA Paris	Baptiste PAUGET Ansys	Table ronde 2
		Type-based Program Synthesis of Controller Logic using the Combinatory Logic Synthesizer	Toward a density semantics for ProbZelus	Memory aspects of SCADE compilation	*New trends for Synchronous languages in spec, impl & efficient run*
9h45-10h30		Oussama Oulkaid, U. Grenoble	Baptiste De Goer & Florence Maraninchi, *VERIMAG* Grenoble	Hugo POMPOUGNAC, INRIA Paris/Sophia.	
		Applying Formal Verification Techniques to Circuit Electric Verification	*But where has all the memory gone?*	Specification and compilation of embedded neural networks	
10h30-11h	*Opening session (organizers)*	Break	Break	Break	Break
11h-11h45	Marc Pouzet, ENS Ulm Paris	Robert de Simone,INRIA Sophia	*Table ronde 1*	*Basile PESIN - INRIA Paris*	*Table ronde 3*
	A look back at Synchronous Lang. contributions	Logical Time in Formal system design: a syncretic view	*Future Directions for Embedded system Research*	Activation structures in a verified compiler	*Next Synchron workshop: perspectives*
11h45-12h30	*Erwan Jahier - VERIMAG Grenoble*	*Reinhard von Hanxleden, U. Kiel*		*Marc POUZET ENS ULM Paris*	**Closing session**
	Self-Stabilizing Algorithms in Lustre	A Synchronous View on Behavior Trees		Hybrid Systems simulation with transparent asserts	
14h	Lunch	Lunch	Lunch	Lunch	Lunch
14h-14h45	*Vincent BREGEON AIRBUS*	*Benoît Caillaud, INRIA Rennes*	Social event Trek in Esterel peaks or shore	*Paul JEANMAIRE (INRIA Paris)*	
	FCGS applicative software sequencing using latency requirements	Modular structural analysis of DAE systems: Is separate compilation of Modelica a forlorn hope?		Denotational semantics for Velus	
14h45-15h30	*Timothy BOURKE, Inria Paris*			*Arseniy GROMOVOY INRIA Sophia*	
	Simple constraints for multi-periodic latency chain			Temporal dysfunction safety analysis using logical time constraints	
16h	Break	Break		Break	
16h-16h45	*Michael MENDLER (Bamberg U.)*	Amos ROBINSON		*Xavier Nicollin- INRIA Grenoble*	
	Stream arrows &lenses for dataflow programming in Haskell	Applicative invariants in Lustre		*Automatic distribution of synchronous programs*	
16h45 - 17h30	*Andreas ZIMMERMANN (Bamberg U*	*Frédéric FORT, IRT Saint-Exupery*			
	Deterministic Policy-synchronised Shared Memory in Rust	*Programming adaptative real-time systems*			
19h30	Dinner	Dinner	Gala Dinner	Dinner	