

# Iterative Bounded Synthesis for Efficient Cycle Detection in Parametric Timed Automata

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<sup>3</sup>Aarhus University, Denmark

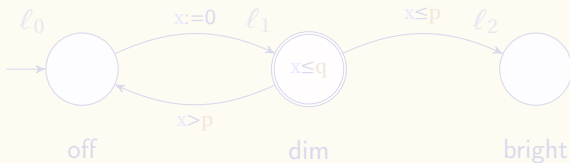
SynCoP, 2 April 2022

## Ingredients of PTA

(Alur, Henzinger, Vardi '93)

- ▶ Finite number of locations  $l_0, l_1, l_2$  ..... transitions in between
- ▶ Clocks  $x, y, z$  ..... advance at the same rate
- ▶ Guards, Invariants, Clock resets ..... specify timing constraints
- ▶ Parameters  $p, q, r$  ..... unknown constants used in constraints

Example: the light switch

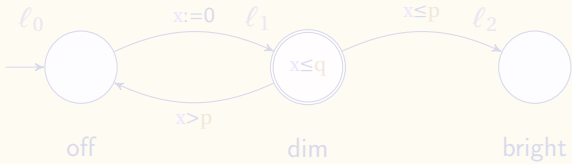


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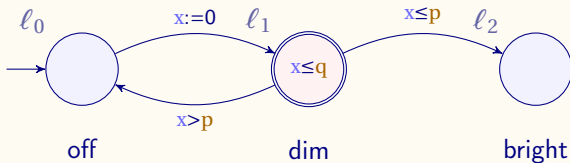


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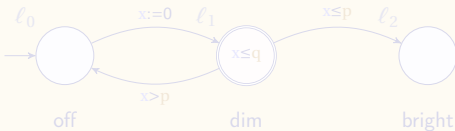
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## Parametric Synthesis

- ▶ Specification and Verification of Real-time systems
- ▶ Timing parameters unknown (at design time)
- ▶ Goal: synthesise parameter constraints for which requirements hold
- ▶ Here: Liveness properties – Büchi conditions

Example: there is an infinite accepting run if and only if  $q > p$



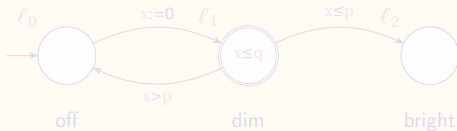
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- ▶ The problem is undecidable for PTA
- ▶ How to search an infinite state space for cycles?
- ▶ Examples and Semi-algorithms – completeness (in the limit)

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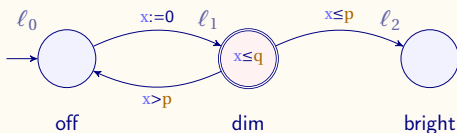
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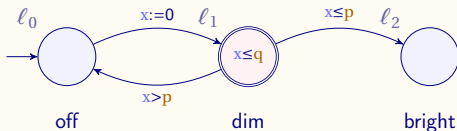
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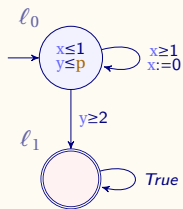


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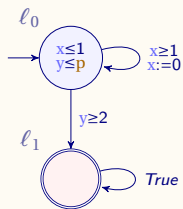


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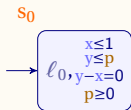


- ▶ Exploration using the self-loop on  $l_0$ :  
Infinite branch without accepting cycle
- ▶ Accepting first strategy:
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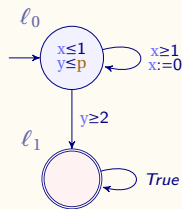
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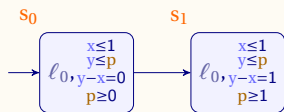
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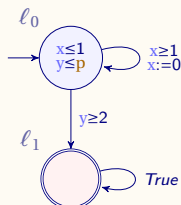


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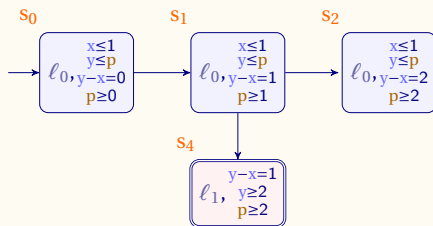


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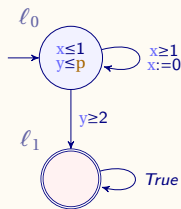


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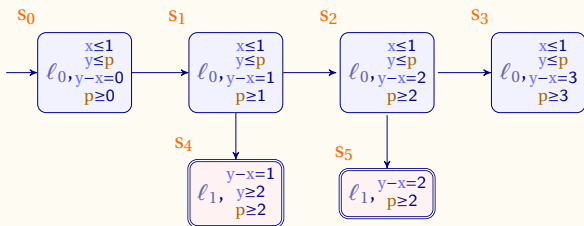


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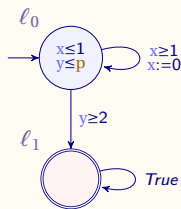


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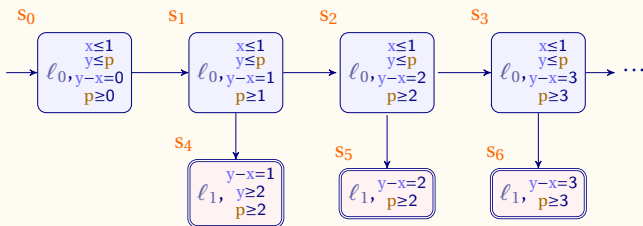


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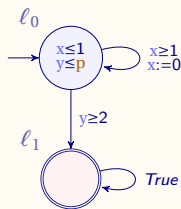


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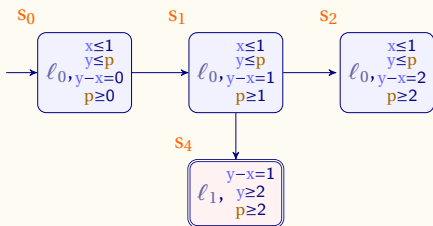


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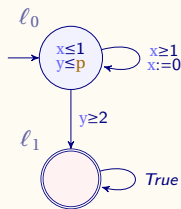


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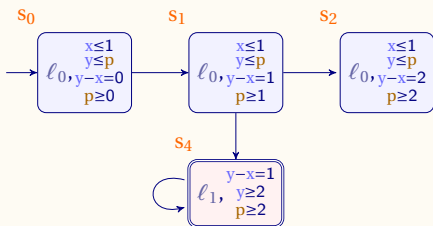


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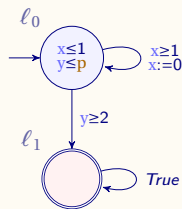
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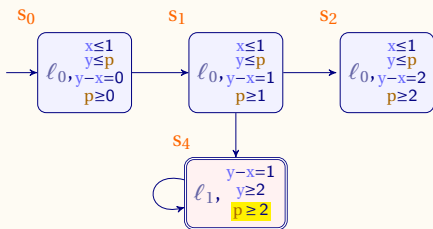
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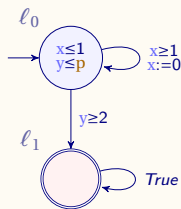


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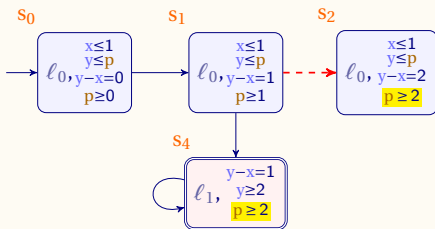


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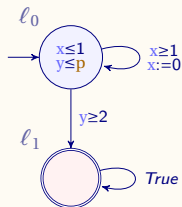
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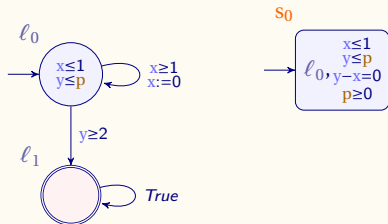
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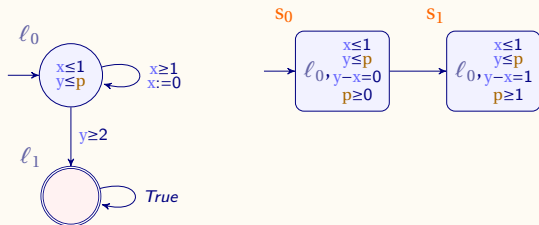
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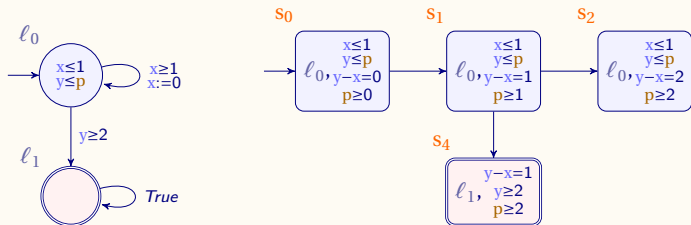
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- ▶ The constraints zones decrease along a path
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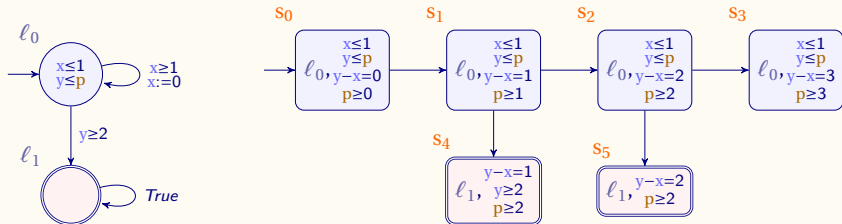
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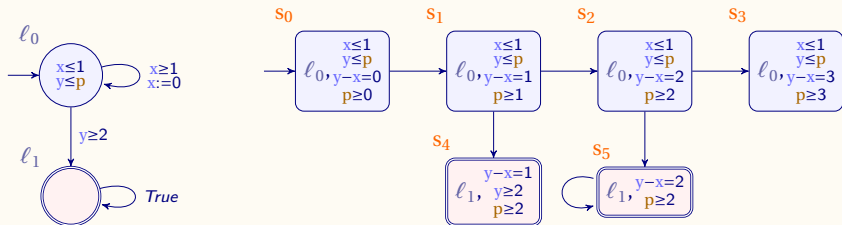
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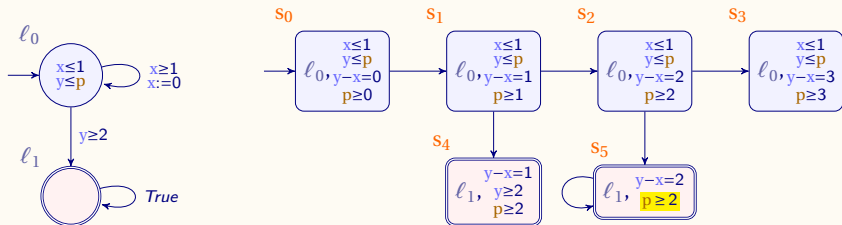


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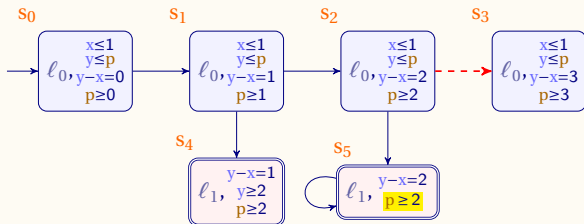
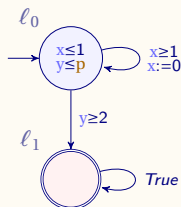


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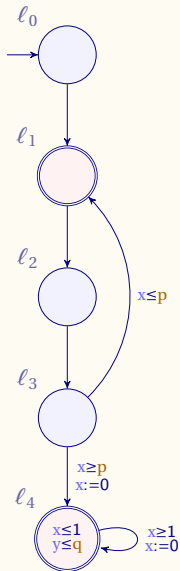




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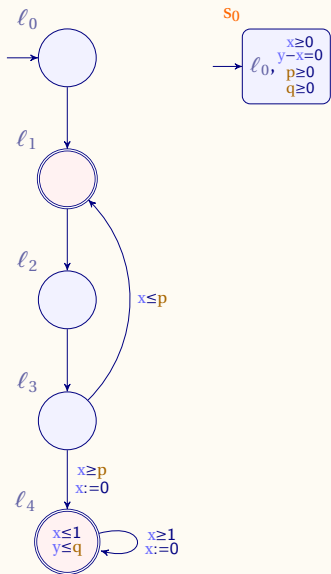


From  $s_1$ , it is possible to engage in an infinite run  $s_1 \rightarrow s_2 \rightarrow s_3 \rightarrow s_2 \rightarrow s_3 \rightarrow \dots$  not a lasso!

Look-ahead strategy:  $s_3$  has an accepting successor "on the stack"

Constraint  $p \geq 0 \wedge q \geq 0$  is synthesized immediately

Cumulative pruning prunes away the infinite run starting in  $s_1$



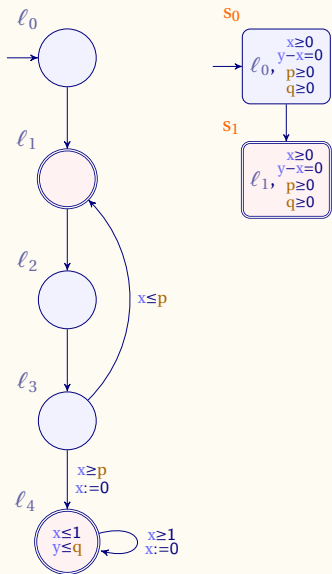
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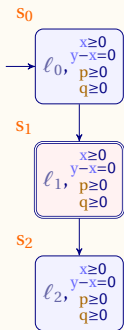
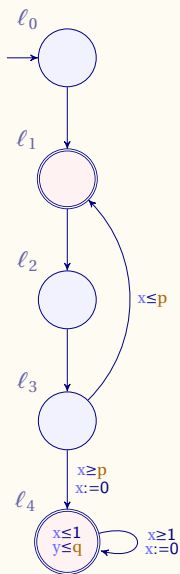
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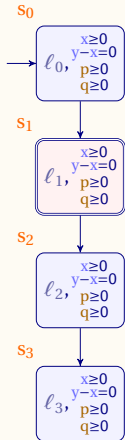
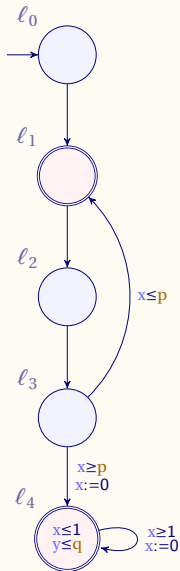
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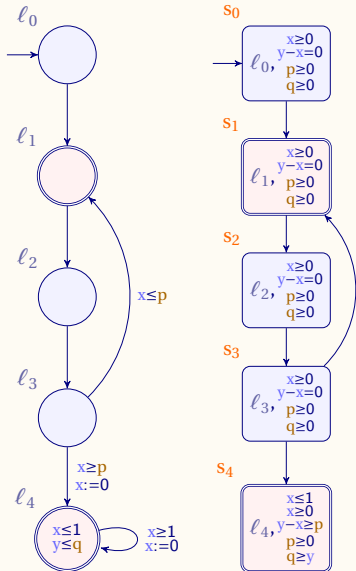
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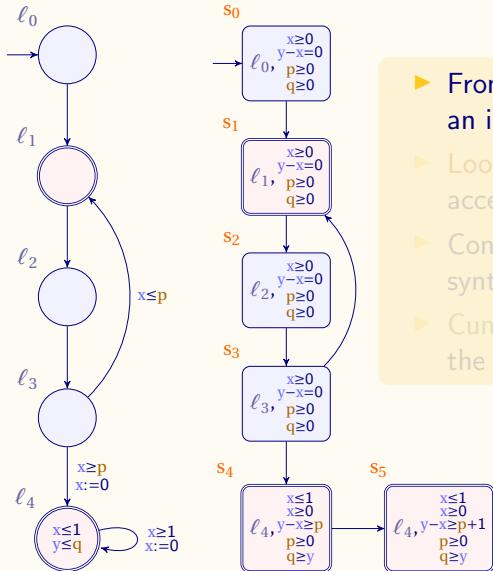
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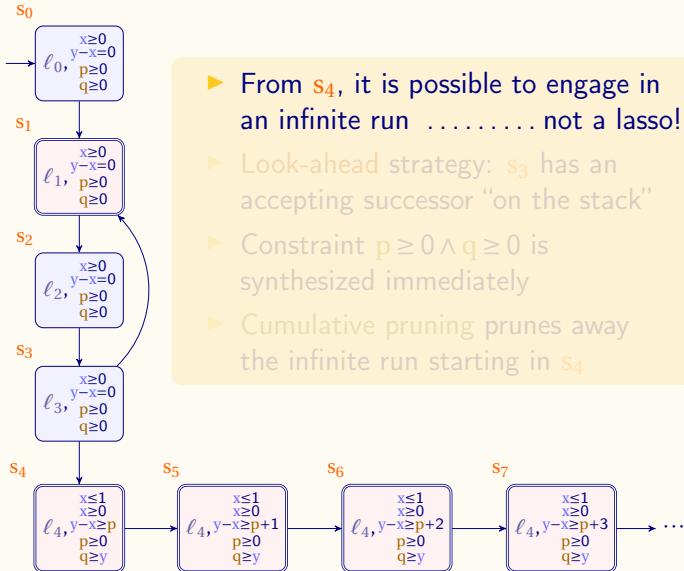
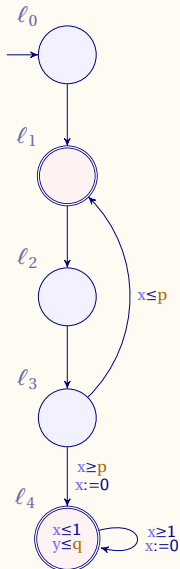
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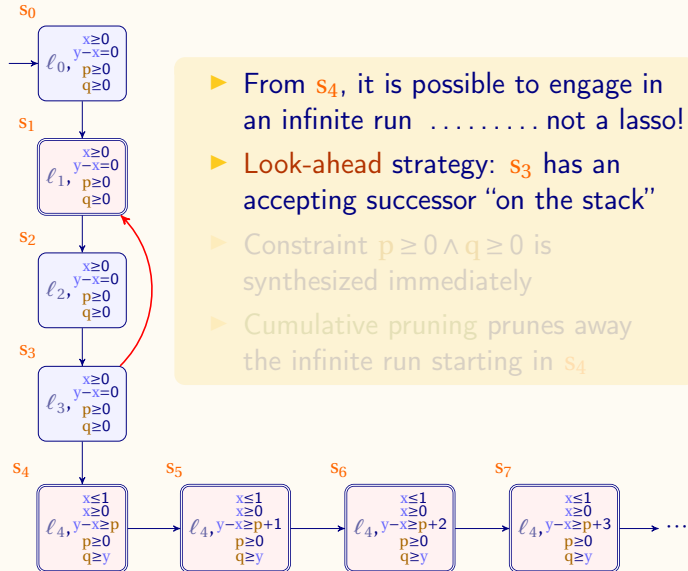
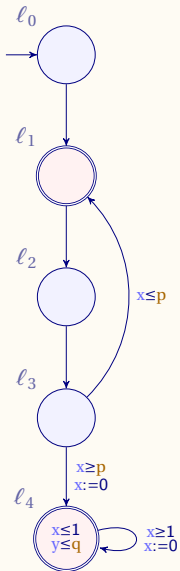
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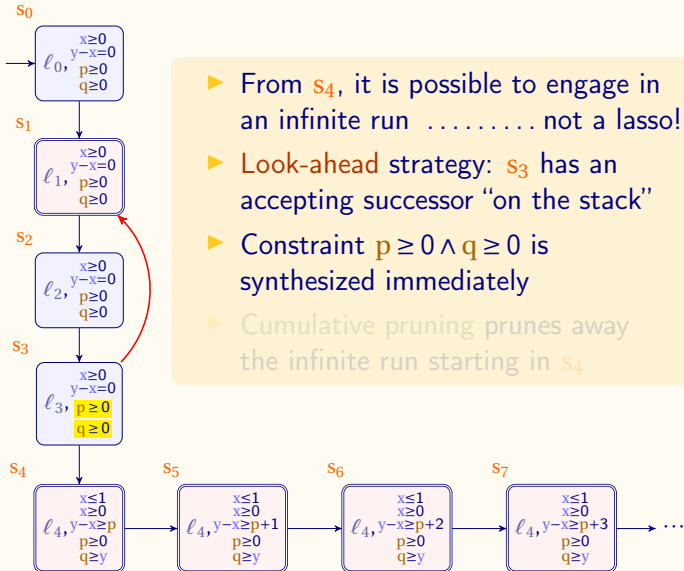
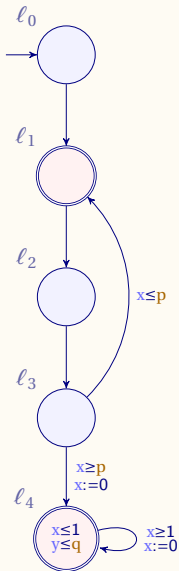
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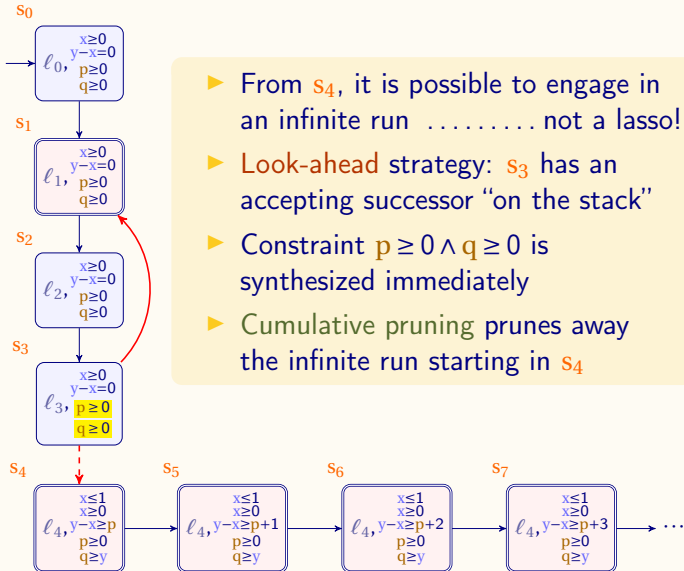
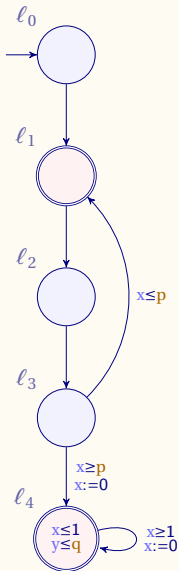


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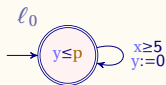
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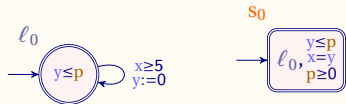


- ▶  $s_1$  is subsumed by  $s_2$ , so  $s_2$  simulates  $s_1$

There must be an infinite accepting run when  $p \geq 5!$   
(but there is no “accepting lasso”)

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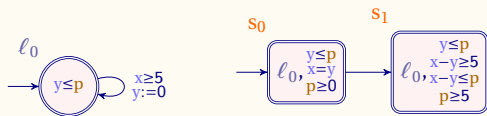


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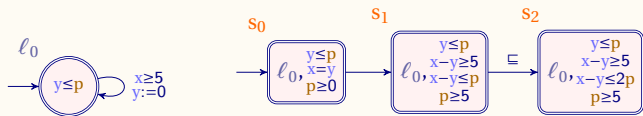
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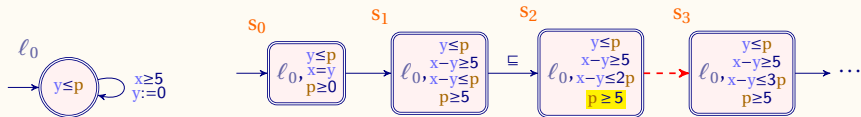
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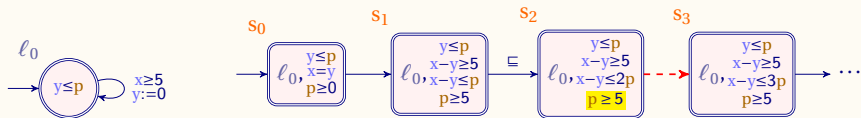




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(too weak)

- ▶ **Partial Soundness:** if the algorithm terminates, then all parameters within the generated constraints lead to accepting runs
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## Correctness in the limit

(ideal)

- ▶ **Soundness in the limit:**  
the algorithm only enumerates constraints that lead to accepting runs
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(realistic)

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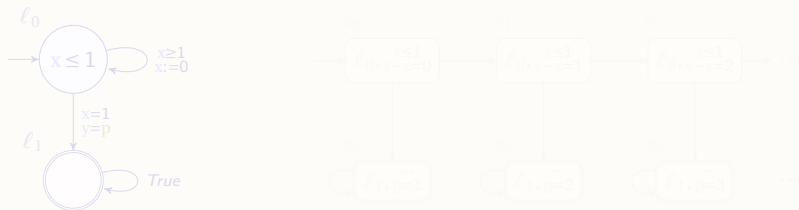
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## Solution set is not a finite set of convex polyhedra



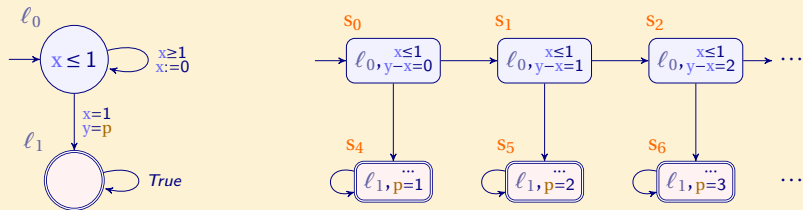
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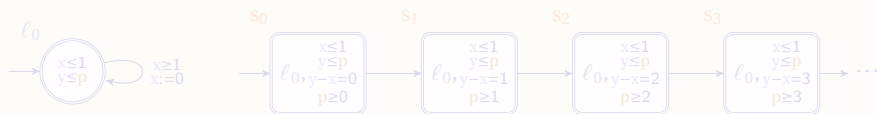
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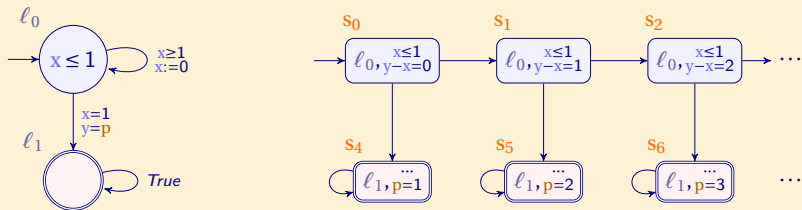
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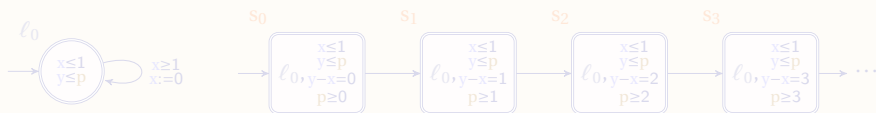
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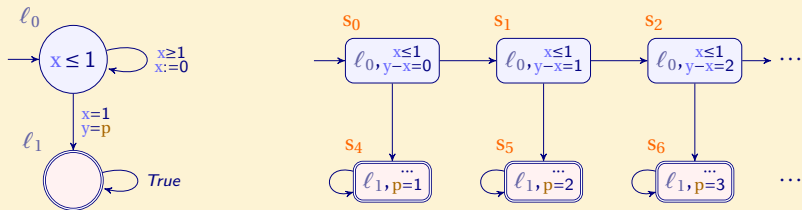
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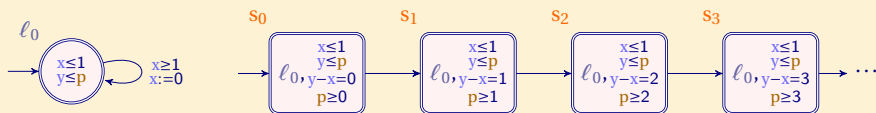
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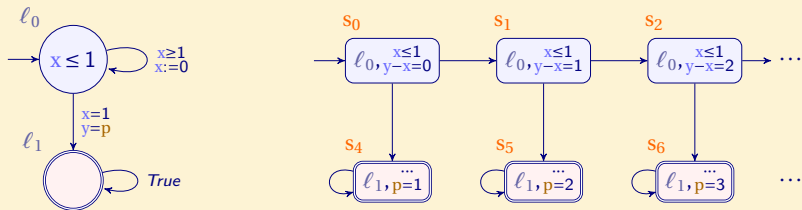
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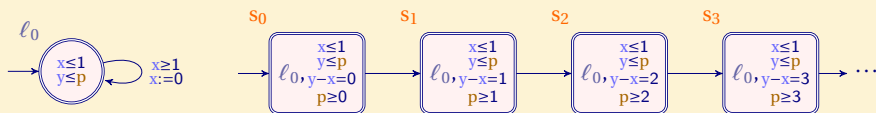


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Algorithm	terminates	partially sound	partially complete	sound in the limit	complete in limit	complete for lassos	$\mathcal{A}_1$	$\mathcal{A}_2$	$\mathcal{A}_3$	$\mathcal{A}_4$	$\mathcal{A}_5$	$\mathcal{A}_6$
NDFS + strategies	×	✓	✓	✓	×	×	✓	×	×	(✓)	(L)	×
BFS + SCC	×	✓	✓	✓	×	✓	✓	×	×	(✓)	(✓)	×
Bound + deepening	×	✓	✓	✓	×	✓	✓	✓	×	(✓)	✓	×
Bounded (fixed $n$ )	✓	✓	×	✓	×	×						
Naïve enumQ	×	(✓)	(✓)	✓	✓	✓						

Naïve enumeration:

- ▶ Enumerate all rational parameter values ..... (Cantor)
- ▶ Check the resulting Timed Automaton for cycles ..... (Alur & Dill)



## Sender

- ▶ requests to transmit a large data package (size  $N=2$ )
- ▶ transmits data, with alternating bit and indications (lossy channel)
- ▶ retransmits messages not acknowledged (bound  $MAX=2$ )

## Receiver

- ▶ reports the received packets to the other user
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The channels will not be used simultaneously

Exact constraint (2 s):  $TS > 2 * TD$  (R1)

The receiver starts each session with a frame with “first-bit” indication

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$$SYNC + TS \geq TR + TD \ \& \ TS > 2 * TD \ \& \ TR > 4 * TS + 3 * TD$$

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